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# Artificial Neural Network Based Adaptive Control of Single Phase Dual Active Bridge With Finite Time Disturbance Compensation

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**ABSTRACT** Single phase Dual Active Bridge (DAB) has found numerous applications in modern energy architectures such as direct current (DC) microgrid, electrical vehicle charging and high voltage direct current (HVDC) system. Due to the model complexities of DAB, this work proposes a model free adaptive control method based on artificial neural network (AANN) which is capable of adjusting the weights online in finite time. The finite time learning property of the proposed controller makes it perfectly robust for the compensation of the disturbances due to source and load side variations. A proportional integral (PI) controller is used to stabilize the nominal dynamics of the system along with the AANN controller. The structure of the proposed controller is as simple as PID controller and as robust as any nonlinear control method. The AANN-PI controller is implemented on TI Launchpad (TMS320F28379D) with a 50 Watts laboratory scale DAB test bench. Finally, the performance of the AANN-PI method is compared experimentally with classical PI and sliding mode controllers.

**INDEX TERMS** Artificial neural network, control system, dual active bridge, dc-dc converters.

## I. INTRODUCTION

The abundant utilization of fossil fuels such as coal, oils and natural gases for energy generation has caused enormous pollution problems. In order to meet the future energy demands of the human beings, alternative and clean energy sources have been explored such as wind, solar and geothermal etc. Therefore, trend of traditional utilization of energy is converting to the renewable energy and resources. The renewable energy sources have intermittent nature so these sources are integrated to ac and dc grids through power converters. Moreover, when the power from the renewable energy sources is not at its peak, then energy storage systems are utilized to ensure smooth power to the consumers. In order to enable the bidirectional power flow between the energy storage and the microgrid, dual active bridge is utilized [1], [2]. In addition to the above, the rise in environmental pollution is compelling the global manufacturers to develop electrical

vehicles (EV) [3]. The key element of energy transferring in electric vehicles is the bidirectional DC/DC converter. With such enormous applications, the research and development in the bidirectional DC/DC converters is more valuable [4]. A thematic concept of smart grid is shown in Fig. 1.

## A. LITERATURE BACKGROUND

An extensive expendability of the DC-DC converters in smart grids has attracted the attention of researchers towards new topology developments, converter's stability and its high control performance. Due to the intermittent nature of the renewable energy resources, it is common to integrate energy storage system in the modern architecture of the microgrid. The bidirectional power flow is achieved by utilizing the bidirectional DC-DC converters alternatively called as dual active bridge (DAB) converters. DAB converter is connected between the energy storage system and the DC bus. In the event, when the renewable energy sources have surplus energy so the DAB is utilized to extract energy from the

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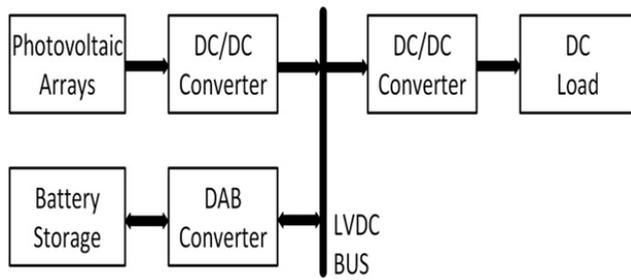


FIGURE 1. Ideal DC microgrid configuration.

DC bus and use it for charging the energy storage and vice versa [5], [6].

Lot of literature is available about the topologies of the DAB converters. However due to intrinsic behavior of these converters for special applications and their topologies, they are divided into isolated and non-isolated bidirectional converters [10]. The non-isolated converters are operated in buck, boost or buck-boost mode where it can be utilized for unidirectional power flow applications. Besides non-isolated converters, the fly-back converter, half H-bridge converter, push-pull and full H-bridge converters are characterized as bidirectional isolated converters. The isolated bidirectional H-bridge converters are more convenient to use in applications like microgrid, un-interruptable power supplies and specifically V2G interface etc. To operate the isolated bidirectional dual active bridge (IBDAB), different modulation techniques are reported by researchers in literature [11]–[13].

Among different modulation techniques, conventional single phase shift (SPS) based modulation technique is very easy to implement. In SPS method a single variable  $\delta$  is used as a phase shift angle between the primary and secondary bridge of IBDAB to control the flow of power. To implement SPS, one degree of freedom variable is required. SPS has some inherent disadvantages such as the existence of circulating currents, zero voltage switching for a low range of the total period and losses. A two and three degree of freedom modulation topology called dual phase shift (DPS) and triple phase shift (TPS) methods have been reported in [14], [15]. Although the DPS and TPS methods show more advantages as compared to the SPS; however, these methods are more complex to implement. Moreover, the complex control system is required to generate more control variables. As an example, the DPS method requires two control variables to control the power flow i.e. phase shift angles  $\delta_d$  and  $\delta_s$ .  $\delta_d$  is the phase shift angle incorporated between the complementary switches of the primary bridge; whereas, the outer phase shift  $\delta_s$  corresponds to the phase angle between the primary and secondary side bridges of IBDAB.

The closed loop DAB performance and stability depends on feedback control system. Different literature has been reported regarding the closed loop control of bidirectional dual active bridge. A robust control system for DAB leads to a stable bus voltage, good battery management and overall fast

dynamic systems response. In [16]–[18], the conventional PI controllers are reported for bidirectional dual active bridge. However uncertainties and variations in system dynamics can cause instability and poor performance of the controller. In [19], a flatness based controller is proposed for DAB converter however the controller structure is more similar to PID. In [20], a fuzzy logic system based control scheme is proposed for dual active bridge. The proposed controller is fixed and so it is not robust against the source and load side variations. In [21], a predictive phase shift control method is proposed. A hybrid controller based on ant clone and BP neural network method has been reported in [15]. Similarly in [22], [28], [29], a fixed weights neural controller is reported to control the power flow between a source and a hybrid vehicle. The reported controller in [15] is a type of fixed controller and will take more resources when implemented on processor. In [23], [24], robust sliding mode controllers are proposed based on the dynamic model of the DAB. The derived model is based on the 1st harmonic of the Fourier series of the square wave and neglects the other harmonic states. In addition, [23], [24] replaces the signum function by a continuous approximation i.e.  $\tanh$  so the robustness of the control system is compromised.

## B. MOTIVATION AND INNOVATION

The aim of this study is to design a close loop control for high high voltage tracking side of DAB. The closed loop control is developed by combination of the proportional-integral (PI) control and single neuron adaptive control (AANN) system. The PI controller is used as a base controller and it stabilizes the nominal dynamics of the DAB converter. With source and load variations, PI controller is not robust enough so the AANN controller is used for the compensation of the disturbance terms induced as a result of the mentioned variations. Weights of the proposed control system are tuned online using Hebbian algorithm [25]. The proposed control system is easy to implement as it doesn't need complex system modeling and exhibits robustness to source and load variations. Moreover, due to the finite time adaptation of the weights, the proposed controller compensates for the variations in finite time.

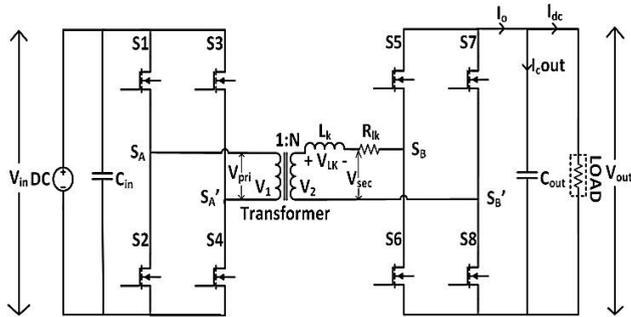
## C. ORGANIZATION

This paper is organized as following. In Section 2, the basic configuration and waveforms of bidirectional dual active bridge using SPS are explained. In section 3, the mathematical modeling of the converter is derived. Closed loop control and basic derivations are included in section 4. The experimental results are discussed in section 5. In the last section the research is concluded.

## II. THE DUAL ACTIVE BRIDGE DC/DC CONVERTER

### A. BASIC PRINCIPLE AND OPERATIONS

The general block diagram considered for this study is shown in Fig. 2. The IBDAB converter consists of switching devices ( $S_1$ - $S_8$ ), with a high frequency transformer connected



**FIGURE 2.** Circuit configuration of bidirectional dual active bridge converter.

in between the two bridges. The two bridges are connected at the primary and secondary sides of the transformer such that one acts as input bridge (on primary side) and the other as output bridge (on secondary side), respectively. Whereas, the transformer provides the galvanic isolation between the two bridges and also serves as a power transferring element in addition with leakage inductor  $L_k$  connected in series. For this study, the IBDAB is connected to variable power supply ( $V_{in}$ ) (varying between 14-24 volts) and variable load. The variable load is switched with fast relay modules operated through microcontroller. Due to symmetry of IBDA topology, the output voltage is controlled only. The proposed control is implemented using SPS method. The switching devices ( $S_1$ - $S_8$ ) are operated with fixed 50 percent duty cycle. The phase shift  $\phi$  between the primary and secondary H-bridges determines the amount of power to be transferred. Considering loss less high frequency transformer, the power can be transferred from primary to secondary side using (1) [23].

$$P_O = \frac{V_{in}V_{out}}{2Nf_sL_k}d(1-d) \quad (1)$$

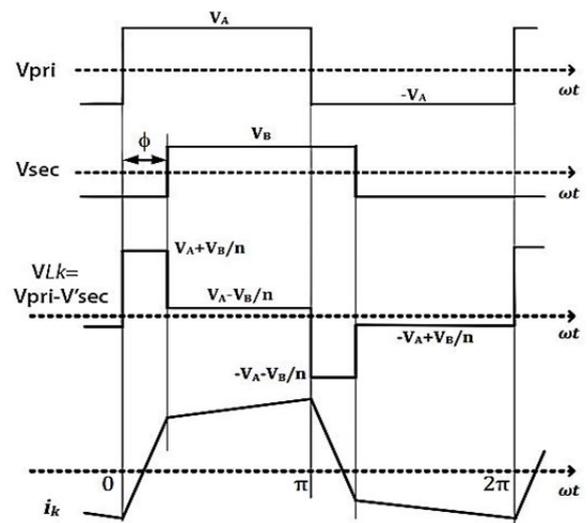
where,  $L_k$  is the leakage inductance of the inductor,  $f_s$  is the switching frequency and  $N$  represents the turn ratio of the transformer. In addition,  $d = \delta/\pi$  is the phase shift ratio;  $V_{in}$  and  $V_{out}$  are the input and output voltages, respectively.

### B. GENERAL WAVEFORM OPERATION

In order to transfer power from the low voltage to high voltage side, the bridge at the primary side is operated with leading phase shift with respect to the bridge at secondary side. Fig. 3 shows the operating waveform when power is transferred from the primary ( $V_{pri}$ ) to secondary ( $V_{sec}$ ) side.

### III. MATHEMATICAL MODELING

In this section, detailed mathematical model for DAB converter is derived which is based on first harmonic of the Fourier series of the square wave. Fig. 3 explains the basic working of the SPS modulation. In each bridge switches are operated with 50 percent duty cycle with a phase shift  $\delta$  between the two modulating wave forms. In order to analyze the dynamics of the DAB, accurate mathematical models are



**FIGURE 3.** Single phase shift (SPS) modulation operating waveforms.

required. The mathematical model is derived by relating the MOSFET's leg switching function into Fourier series with equivalent harmonic representations. The switching functions of leg (A) are given as following [26].

$$S_A = \frac{1}{2} + \frac{2}{\pi} \sum_{n=0}^{\infty} \frac{\sin(2n+1)(\omega_s t)}{2n+1} \quad (2)$$

$$S'_A = S_A \left( t - \frac{\pi}{\omega_s} \right) \quad (3)$$

From Fig. 2,  $V_1 = V_{pri}$  and  $V_2 = NV_1$ , where  $N$  is the transformer's turn ratio. The expression for the primary voltage is written as following.

$$V_{pri} = V_{in} [S_A - S'_A] \quad (4)$$

$$V_{pri} = V_{in} \frac{4}{\pi} \sum_{n=0}^{\infty} \frac{\sin(2n+1)(\omega_s t)}{2n+1} \quad (5)$$

Similarly, the switches of high side bridge are operated with phase delay ( $\delta$ ), so the switching function of leg B with phase delay is expressed as following.

$$S_B = \frac{1}{2} + \frac{2}{\pi} \sum_{n=0}^{\infty} \frac{\sin(2n+1)(\omega_s t - \delta)}{2n+1} \quad (6)$$

The secondary side voltage can be written as,

$$V_{sec} = V_{out} [S_B - S'_B] \quad (7)$$

$$V_{sec} = V_{out} \frac{4}{\pi} \sum_{n=0}^{\infty} \frac{\sin(2n+1)(\omega_s t - \delta)}{2n+1} \quad (8)$$

The voltage expression for the high voltage side of the transformer is expressed as following,

$$R_k i_{lk}(t) + L_k \frac{di_{lk}(t)}{dt} = Nv_{pri}(t) - V_{sec}(t) \quad (9)$$

Here,  $N = \frac{N_s}{N_p}$  represents the transformer turn ratio. By putting (5) and (8) in (9), one obtains,

$$N \left[ V_{in} \frac{4}{\pi} \sum_{n=0}^{\infty} \frac{\sin(2n+1)\omega_s t}{2n+1} \right] - V_{out} \frac{4}{\pi} \sum_{n=0}^{\infty} \frac{\sin(2n+1)(\omega_s t - \delta)}{2n+1} = R_k i_{lk}(t) + L_k \frac{di_{lk}(t)}{dt} \tag{10}$$

By re-arranging (10), the expression for  $i_{lk}$  is obtained as,

$$i_{lk}(t) = \frac{4}{\pi} \sum_{n=0}^{\infty} \frac{1}{(2n+1)} \times \left[ \begin{array}{l} \frac{NV_{pri}}{|Z(n)|} \sin(2n+1)\omega_s t - \varphi_z(n) \\ - \frac{V_{sec}}{|Z(n)|} \sin(2n+1)(\omega_s t - \delta) - \varphi_z(n) \end{array} \right] \tag{11}$$

$$|Z(n)| = \sqrt{R_{lk}^2 + [(2n+1)\omega_s L_k]^2} \tag{12}$$

$$\varphi_z(n) = \tan^{-1} \left( \frac{(2n+1)\omega_s L_k}{R_{lk}} \right) \tag{13}$$

Here  $\omega_s$  is the switching frequency,  $|Z(n)|$  represents the impedance of the two bridges and  $\varphi_z(n)$  is the corresponding phase angle. By applying Kirchoff's current law (KCL) at the load side node of the DAB converter of Fig. 2, one obtains,

$$I_o = I_{cout} + I_{dc} \tag{14}$$

$$I_{cout} = I_o - I_{dc} \tag{15}$$

By re arranging (15) and including the dynamics of the switching functions, the following expressions are obtained.

$$C \frac{dV_{cout}}{dt} = I_o - I_{dc} \tag{16}$$

$$C \frac{dV_{cout}}{dt} = I_{lk}(S_B - S'_B) - I_{dc} \tag{17}$$

$$C \frac{dV_{cout}}{dt} = I_{lk}(S_B - S'_B) - \frac{V_{cout}}{R_{load}} \tag{18}$$

By combining (11), (7) and (18) one obtains

$$\begin{aligned} & \frac{dV_{cout}(t)}{dt} \\ &= -\frac{V_{cout}(t)}{R_{load}} \\ &+ \frac{1}{C_{out}} \left[ \frac{4}{\pi} \sum_{n=0}^m \frac{NV_{in}}{(2n+1)|Z(n)|} \sin \left( \begin{array}{l} (2n+1)\omega_s t \\ -\varphi_z(n) \end{array} \right) \right. \\ & \quad \left. - V_{out} \sin \left( \begin{array}{l} (2n+1)\omega_s t - \delta \\ -\varphi_z(n) \end{array} \right) \right] \\ &+ \frac{4}{\pi} \sum_{r=0}^1 \frac{1}{(2r+1)} \sin((2r+1)(\omega_s t - \delta)) \end{aligned} \tag{19}$$

From [23], (19) can be re written as (20)

$$\begin{aligned} \frac{dV_{cout}}{dt} &= \frac{-8}{C_{out}\pi^2} \sum_{n=1,3,5,\dots}^{\infty} \frac{\cos(\varphi_z(n))}{n^2|Z(n)|} V_{out} \\ &+ \frac{8}{C_{out}\pi^2} \sum_{n=1,3,5,\dots}^{\infty} \frac{\cos(n\delta - \varphi_z(n))}{n^2|Z(n)|} NV_{in} - \frac{I_{dc}}{C_{out}} \end{aligned} \tag{20}$$

In (20),  $N$  represents the turn's ratio of the transformer whereas;  $n$  refers to number of harmonic state. By placing  $n = 1$  yields the state space model of the DAB based on 1st harmonic of the Fourier series and it is expressed as following.

$$\dot{X} = F(x) + G(x)U + D \tag{21}$$

Here the terms  $F(x)$ ,  $G(x)$  and  $D$  are defined as following

$$F(x) = \frac{-8}{C_{out}\pi^2} \sum_{n=1,3,5,\dots}^{\infty} \frac{\cos(\varphi_z(n))}{n^2|Z(n)|} V_{out},$$

$$G(x) = \frac{8}{C_{out}\pi^2} \sum_{n=1,3,5,\dots}^{\infty} \frac{1}{n^2|Z(n)|} NV_{in},$$

$$\begin{aligned} D &= \frac{-8}{C_{out}\pi^2} \sum_{n=3,5,\dots}^{\infty} \frac{\cos(\varphi_z(n))}{n^2|Z(n)|} V_{out} \\ &+ \frac{8}{C_{out}\pi^2} \sum_{n=3,5,\dots}^{\infty} \frac{\cos(n\delta - \varphi_z(n))}{n^2|Z(n)|} NV_{in} \\ &- \frac{I_{dc}}{C_{out}} + d_{load} + d_{source} \end{aligned}$$

The lumped disturbance term  $D$  contains all the higher order harmonics including the disturbance terms due to source variation  $d_{source}$  and the load variation  $d_{load}$ . Therefore, (21) does not represent the accurate nominal dynamics of the DAB converter when other harmonics states are not considered.

#### IV. CLOSE LOOP CONTROL

This section of the paper proposes two controllers. The first controller is based on the ANN, which tunes the gains of the PI controller. The second controller is based on integral sliding mode control. The results of both controllers are compared.

*Artificial Neural Networks:* The artificial neural networks are composed of billions and trillions of neurons which continuously interact with each other and process the information. The ANN is imitated from biological neural networks and is designed as such that the information taken as input is forwarded through nodes to the output. The output of the ANN networks is similarly called an activation function or node value. Following up the artificial single neuron based method as shown in Fig. 4, the gains of the PI controller are tuned. Where,  $x = [x_1 x_2 \dots x_m]^T$  refers to the number of inputs and  $w = [w_0 w_1 \dots w_m]^T$  denote the weights. Using ANN, Hebb based algorithm is followed to tune the

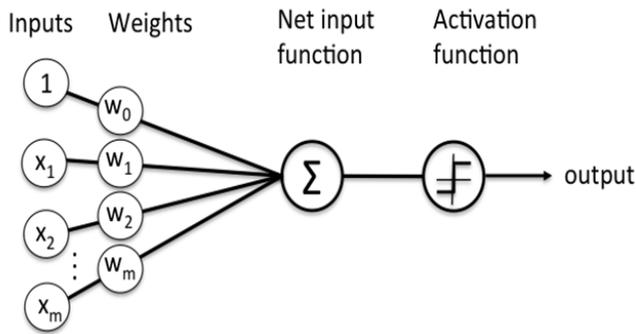


FIGURE 4. Single phase shift (SPS) modulation operating waveforms.

parameters of PI controller, which are briefly discussed in section A.

**A. AANN-PI**

In this section, the proposed control topology is discussed in details. The dynamic model of (21) is based on first harmonic of Fourier series of the square wave. Moreover, the disturbance term D is very complex. Due to the model complexities, this work proposes a model free adaptive control method which is capable of adjusting the weights online in finite time. The finite time learning property of the proposed controller makes it perfectly robust for the compensation of the disturbances due to source and load side variations. The implementation of the proposed control is shown in Fig. 5. General expression for fixed gain proportional, derivative and integral (PID) is written as following.

$$u_{PID}(k) = K_p e(k) + K_i \sum_{n=1}^k e(n) + K_d (e(k) - e(k-1)) \tag{22}$$

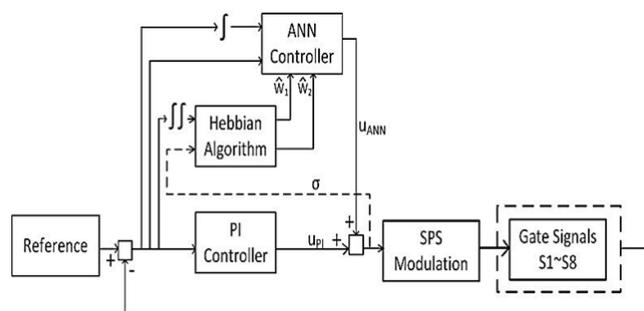


FIGURE 5. Block diagram of proposed adaptive artificial neural networks based PI controller.

Here,  $e$  represents the error between the reference command and feedback signal,  $K_p$  is proportional constant,  $K_i$  represents integral gain and  $K_d$  is the derivative gain [18]. The classical fixed PID control is not robust to the nonlinear tracking commands, source and load side variations. To overcome this limitation and in order to achieve desired tracking response, an auxiliary controller based on adaptive ANN method is

integrated with the classical PI control. The auxiliary controller is a single neuron controller and its weights are tuned online using Hebbian method. The overall control scheme is mathematically represented as following.

$$u_T(k) = u_{PID}(k) + u_{AANN}(k) \tag{23}$$

In (23), PID controller is discretized using Tustin method. Moreover, the second term  $u_{AANN}$  represents the adaptive control scheme based on ANN method. Mathematically the AANN controller is written as following

$$u_{AANN}(k) = \sum_{i=1}^{i=n} W_i(k) x_i(k) \tag{24}$$

In (24),  $W_i$  represent the weights and  $x_i$  is the state vector. By choosing  $x_1(k) = e(k)$ ,  $x_2(k) = \sum_{n=1}^k e(n)$  for  $i = 1, 2, \dots$  and considering  $K_d = 0$  in this case.

Using (22) and (23), (24) is modified as following.

$$u_T(k) = (\hat{W}_1(k) + K_p) x_1(k) + (\hat{W}_2(k) + K_i) x_2(k) \tag{25}$$

In (25),  $\hat{W}_1$  and  $\hat{W}_2$  are the adaptive weights that are tuned online using the following discrete expression of the following form.

$$\hat{W}_i(k) = (\hat{W}_i(k-1) + \eta_i z_i(k) u(k)) \tag{26}$$

In (26),  $z_i(k) = \sum_{n=1}^k \sum_{n=1}^k e(n)$ , and it represents the double integral of error at  $k^{th}$  sample. The  $u(k) = \sigma(k)$  corresponds to the  $k^{th}$  sample of control signal which is phase shift and  $\eta_i$  represents the learning rates of the adaptive algorithm. In addition, from (26) the upper limits of the gain terms are chosen as  $(\hat{W}_1 + K_p) \leq \zeta_1$  and  $(\hat{W}_2 + K_i) \leq \zeta_2$ . All the parameters used in the proposed controller are tabulated in table 2.

**B. INTEGRAL SLIDING MODE CONTROL**

In this section, single integral sliding mode control system is derived based on the mathematical model given in (21). Let  $e = X - X_d$  represents the voltage tracking error, and  $e' = \dot{X} - \dot{X}_d$  be its first derivative, then the sliding surface is defined as following.

$$S_v = C_1 e + C_2 \int e \tag{27}$$

Here  $C_1$  and  $C_2$  represent the gain of the sliding surface. Taking first derivative of (27) and combining it with (21) yields the following equation.

$$S_v' = C_1 F(x) + C_1 D + C_1 G(x) U + C_2 e - \dot{X}_d \tag{28}$$

$$U = -(C_1 G(x))^{-1} \begin{bmatrix} C_1 F(x) + C_1 D + C_2 e \\ -\dot{X}_d - k_r \text{sgn}(S_v) \end{bmatrix} \tag{29}$$

From (29), the phase shift is implemented as  $\delta = \sin^{-1}(U)$  as discussed in [23], [24]. By combining (28), (29) and by letting discontinuous gain  $K_r > D_{max}$ , it is easy to show that  $S_v' \cong 0$ , which shows the reaching condition of the sliding surface.

V. RESULTS AND DISCUSSIONS

A. EXPERIMENTAL SETUP AND PARAMETERS

To verify the control algorithm shown in Fig. 5, a laboratory scaled dual active bridge test bench of 150 watts is developed. The proposed control method is tested under the following three conditions: 1. Variable reference command, 2. Source variation 3. Load variation disturbance.

The proposed controller is implemented using rapid prototyping technique with Simulink interface. The experimental setup is shown in Fig. 6. The parameters of the test bench are tabulated in Table 1 and gains of the controllers are given in Table 2. In addition, the gains of the PI controller are derived for DAB converter using small signal analysis as discussed in [27]. Also, the SMC and ANN gains are determined by trial and error method.

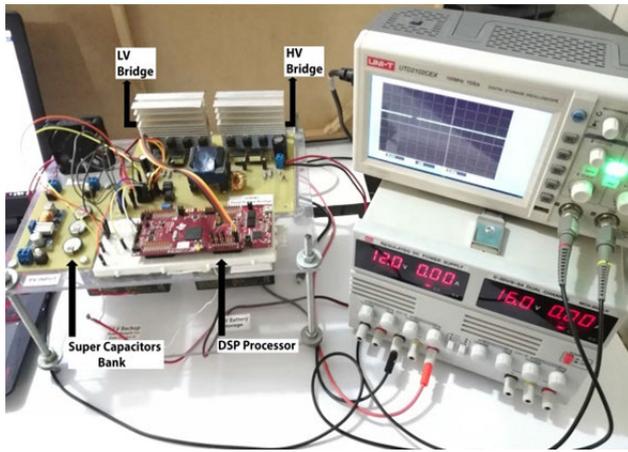


FIGURE 6. Experimental setup of Bidirectional DAB.

TABLE 1. Parameters for experiment.

Parameters	Value
Rated output power	50 watts
Input voltage (Vin)	12V
Output voltage (Vout)	24V
Cin, Cout	100μF, 200μF
N	1:2
L <sub>sk</sub>	50 μH
Load	100 Ah

B. TEST UNDER VARIABLE REFERENCE COMMAND

1) VOLTAGE TRACKING PERFORMANCE WITH PI CONTROLLER

Fig. 7a shows the voltage tracking performance of DAB with fixed gain PI controller under variable reference command. As shown in Fig. 7a, the reference signal is abruptly changed from 24V to 14V at time t = 20ms, then set back to 24V at time t = 120ms and finally switched back to 14V at time t = 220ms. The parameters of the PI controller are shown in Table 2.

TABLE 2. Control system parameters.

Controller	Parameters	Value
PI	Kp	2.5
	Ki	1.5
AANN-PI	$\hat{w}_1(0)$	0.5
	$\hat{w}_2(0)$	0.5
	$\eta_1$	$1e^{-8}$
	$\eta_2$	$1.25e^{-8}$
	$\zeta_1$	12
	$\zeta_2$	14
SMC	$k_r$	100
	C <sub>1</sub>	0.05
	C <sub>2</sub>	30

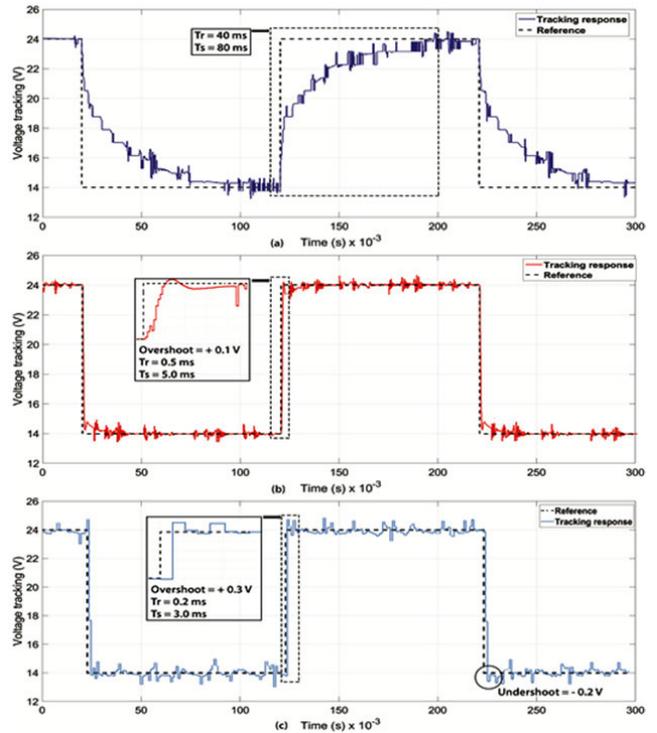


FIGURE 7. Voltage tracking performance of DAB under variable reference command for (a) PI controller (b) AANN-PI controller (c) SMC controller.

The output voltage convergence to its reference command with fixed gain PI controller is very poor. Since there is no overshoot while tracking its reference, the controller converges to the reference in 80ms with rise time (T<sub>r</sub>) of 40ms. Fig. 8 shows the corresponding change in phase shift. The phase shift is taken as PWM register counts. These ranges in between 0 and 250 counts, which correspond to 0° and 90° of phase shift. Fig. 9 shows the output voltage measured using digital oscilloscope. The voltage tracking performance of fixed gain PI is tabulated in Table 3.

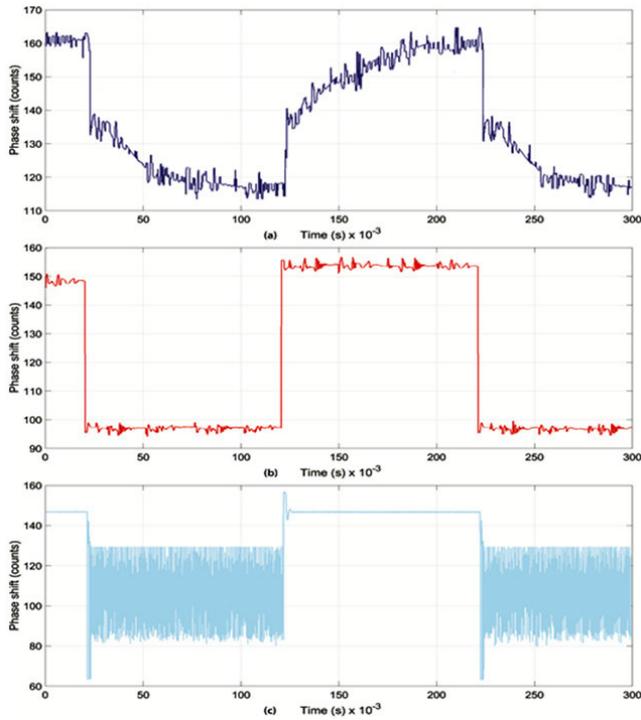


FIGURE 8. Phase shift variation under variable reference command for (a) PI controller (b) AANN-PI controller (c) SMC controller.

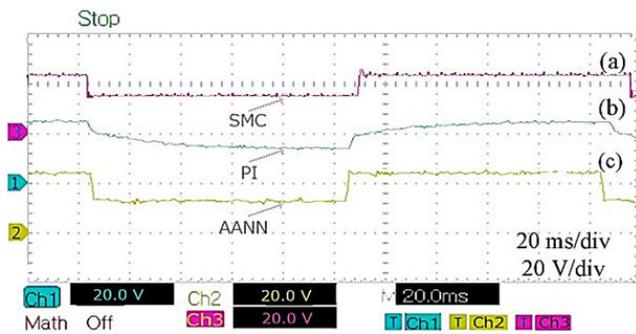


FIGURE 9. Voltage measurement of DAB under variable reference command using digital oscilloscope (a) SMC controller (b) PI controller (c) AANN-PI controller.

2) VOLTAGE TRACKING PERFORMANCE WITH AANN-PI CONTROLLER

The voltage tracking performance of AANN-PI control is shown in Fig. 7b. The proposed controller tracks its reference in finite time with minor overshoot of 0.1V in tracking signal.

The voltage convergence rate of the AANN-PI controller is very fast as compared to the fixed gain PI controller. In addition, settling time of the proposed controller is 5ms with rise time of 0.5ms. Similar results are obtained when the reference voltage is changed back to 14V at 220ms from 24V. Fig. 10 shows the finite time online adaptation of the weights ( $\hat{W}_1, \hat{W}_2$ ) of the neural network controller with respect to the variations in the reference voltage. The voltage tracking performance of AANN-PI method is tabulated in Table 3. (Due to symmetry table 3 only shows the results tabulated based on one disturbance cycle).

TABLE 3. Comparative analysis.

Controller	Response	Reference variation 0<t<300 (ms)	Source variation 0<t<300 (ms)	Load variation 0<t<300 (ms)
PI	Overshoot	0	2.8	2.5
	Undershoot	0	-2.8	-2.3
	Rise time	40	3	4
	Settling time	80	80	80
AANN-PI	Overshoot	0.1	0.9	0.9
	Undershoot	0	-0.8	-0.8
	Rise time	0.5	1.5	2.5
	Settling time	5	9	9
SMC	Overshoot	0.3	0.7	0.7
	Undershoot	-0.2	-0.7	-0.7
	Rise time	0.2	0.2	0.2
	Settling time	3	1	1

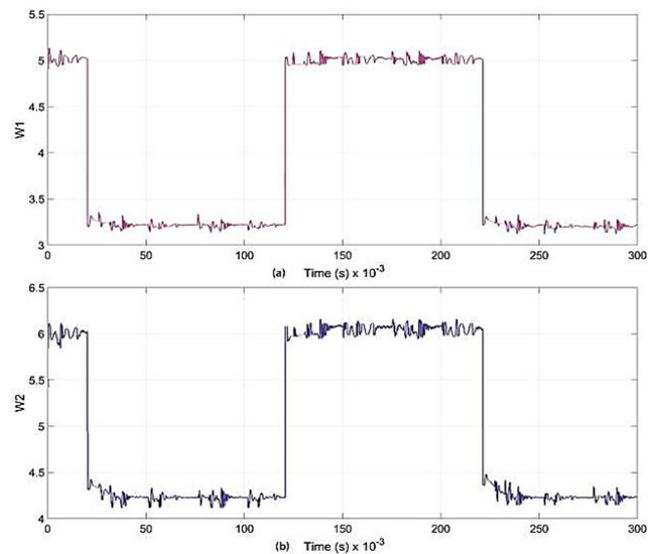


FIGURE 10. Online weight tuning of AANN-PI under variable reference command (a)  $\hat{w}^1$  (b)  $\hat{w}^2$ .

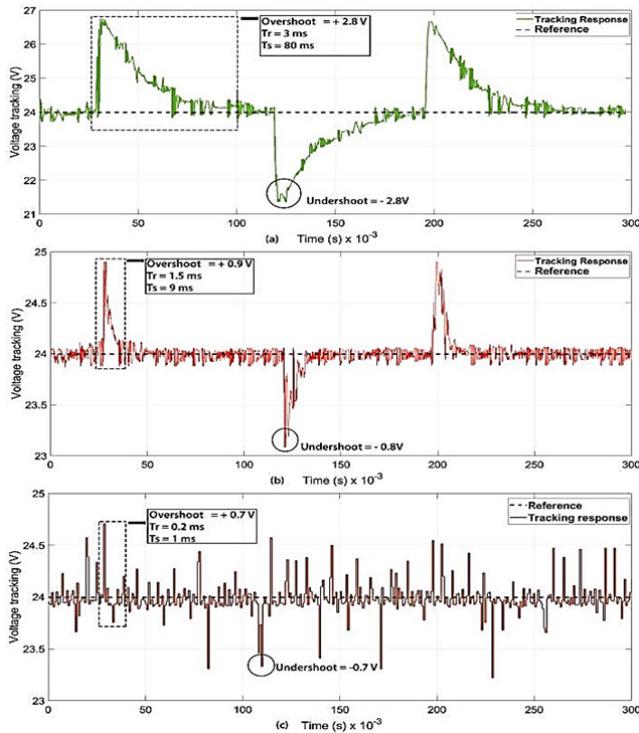
3) VOLTAGE TRACKING PERFORMANCE WITH INTEGRAL SMC CONTROLLER

The voltage tracking performance of integral SMC control is shown in Fig. 7c. The proposed controller tracks its reference in finite time with minor overshoot of 0.3V in tracking the signal. The measured settling and rise times are 3ms and 0.2ms, respectively. Fig. 8c shows the phase shift control signal using the integral SMC control method. From the presented result it is evident the phase shift signal contains high frequency chattering which is not feasible for long period operation of DAB converter. Fig. 9a shows the experimental results collected using digital oscilloscope. (Due to symmetry table 3 only shows the results tabulated based on one disturbance cycle).

C. TEST UNDER VARIABLE SOURCE CONDITION

1) VOLTAGE TRACKING PERFORMANCE WITH PI CONTROLLER

Fig. 11a shows the output voltage regulations with respect to the variations in source voltage. The initial value of source



**FIGURE 11.** Voltage tracking performance of DAB under variable source voltage (a) PI controller (b) AANN-PI controller (c) SMC controller.

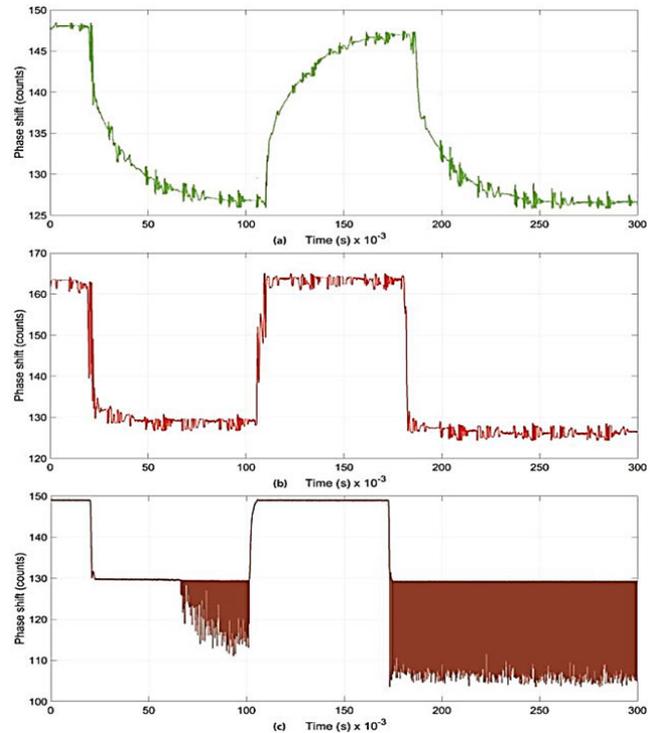
voltage is 12V till time  $t = 20\text{ms}$ . From 120ms onward, the reference voltage is changed to 16V which resulted in an overshoot of 2.8V in the output measured voltage. The output voltage converges slowly to the reference as can be observed in Fig. 11a. The parameters of the test are tabulated in Table 3, which clearly replicate the slow performance of the controller. The corresponding change in the phase shift is shown in Fig. 12a. Fig. 13b shows the experimental results measured using digital oscilloscope.

### 2) VOLTAGE TRACKING PERFORMANCE WITH ANN-PI CONTROLLER

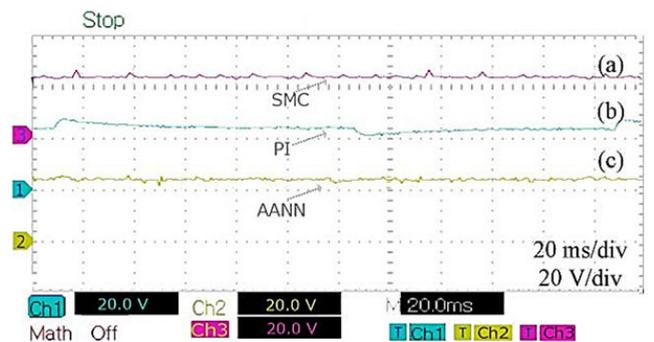
The voltage tracking performance of AANN-PI controller is given in Fig. 11b. As a result in source variations (as given in subsection V B), the measured overshoot of 0.9V and undershoot of -0.8V in the output voltage are observed. Fig. 13c shows the practical results measured through oscilloscope. Fig.12b shows the measured phase shift and Fig. 14 shows the corresponding online adaptation of the weights ( $\hat{W}_1, \hat{W}_2$ ). (Due to symmetry table 3 only shows the results tabulated based on one disturbance cycle).

### 3) VOLTAGE TRACKING PERFORMANCE WITH SMC CONTROLLER

Fig. 11c shows the output voltage regulations with respect to the variations using SMC control method. From the results it is evident that the SMC Control method is robust to the source variations but the tracking signal contains high frequency oscillations. Due to the high frequency oscillations,



**FIGURE 12.** Phase shift variation under variable source voltage (a) PI controller (b) AANN-PI controller (c) SMC controller.



**FIGURE 13.** Voltage measurement of DAB under variable source voltage using digital oscilloscope for (a) SMC controller (b) PI controller (c) AANN-PI controller.

over shoot of 0.7V and undershoots of  $-0.7\text{V}$  are observed in the tracking signal. Fig. 12c shows the phase shift response measured and it shows high frequency chattering. Fig. 13a shows the results measured through oscilloscope. (Due to symmetry table 3 only shows the results tabulated based on one disturbance cycle).

## D. TEST UNDER VARIABLE LOAD CONDITION

### 1) VOLTAGE TRACKING PERFORMANCE WITH PI CONTROLLER

Fig. 15a shows the voltage tracking performance of fixed gain PI controller. The undershoot of  $-2.3\text{V}$  is observed at time  $t = 120\text{ms}$  when the load is suddenly changed from 200 ohms to 500 ohms. Similarly, an overshoot of 2.5V is observed

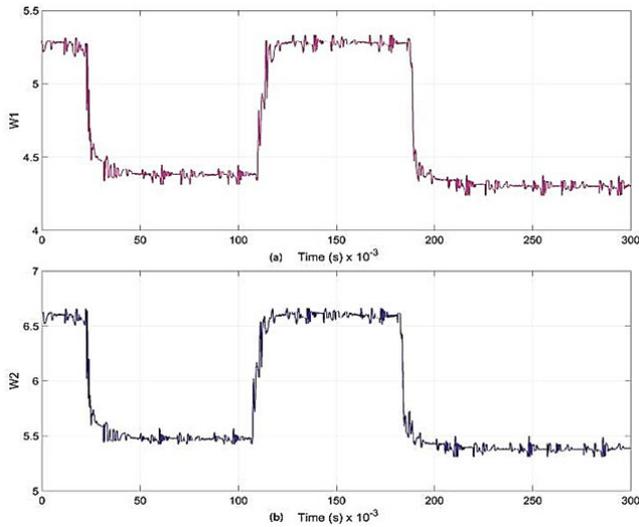


FIGURE 14. Online weight tuning of AANN-PI under variable source voltage a)  $\hat{w}_1$  b)  $\hat{w}_2$ .

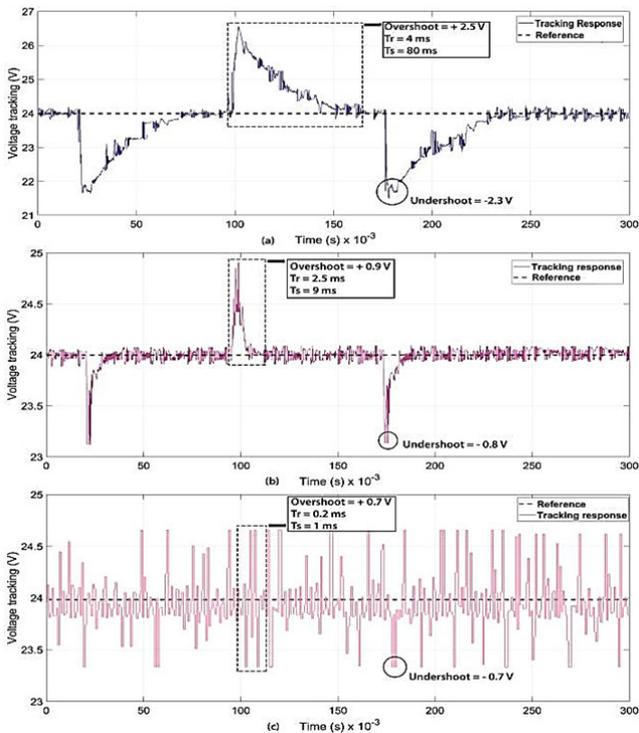


FIGURE 15. Voltage tracking performance of DAB under load variation for (a) PI controller (b) AANN-PI controller (c) SMC controller.

at time  $t = 80\text{ms}$  when the load is restored to 200 ohms. Fig. 17b shows the measured tracking signal using digital oscilloscope. The corresponding phase shift signal is shown in Fig. 16a. Table 3 shows the performance tabulation of the system with fixed gain PI controller under load variations.

### 2) VOLTAGE TRACKING PERFORMANCE WITH AANN-PI CONTROLLER

Fig. 15b shows the AANN-PI based controller voltage tracking response with finite time adaptation of the weights.

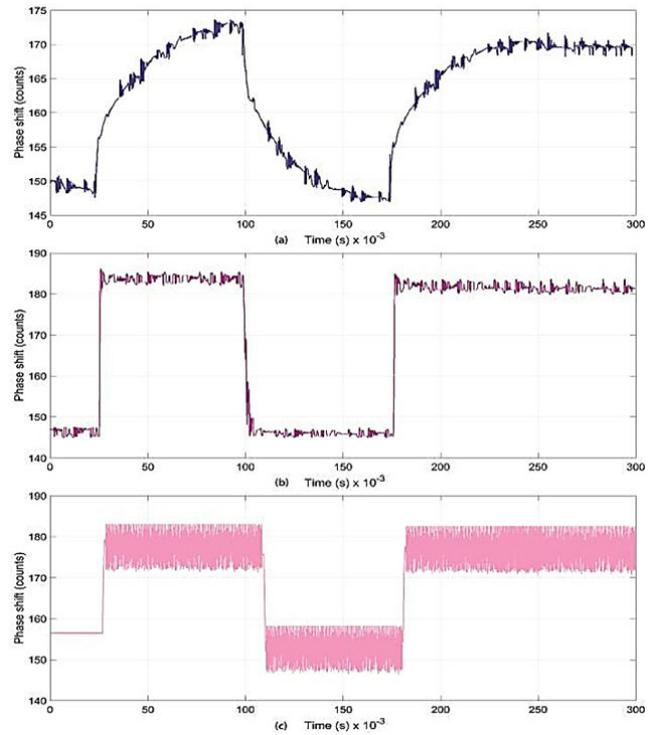


FIGURE 16. Phase shift variation under load variation for (a) PI controller (b) AANN-PI controller (c) SMC controller.

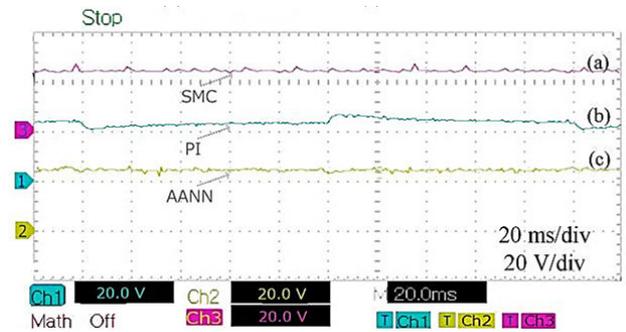
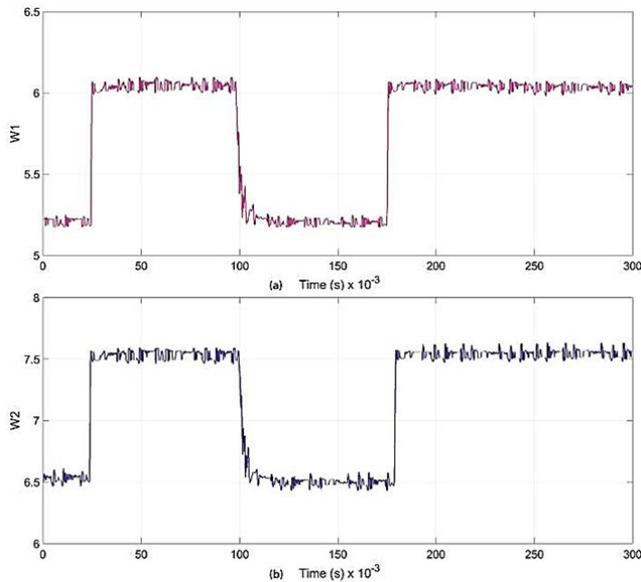


FIGURE 17. Voltage measurement of DAB under variable load using digital oscilloscope for (a) SMC controller (b) PI controller (c) AANN-PI controller.

The controller exhibits robustness to the load variations and tracks the desired reference with overall settling time of 9ms. The rise time response of the controller is far better than PI controller which contributes to an accurate and fast reference tracking. Fig. 17c shows the measured voltage signal using digital oscilloscope. The corresponding change in phase shift is shown in Fig. 16b. With finite time disturbance compensation at 20ms, the controller changes the phase shift from 160 to 180 with the online weights adjustments of  $\hat{W}_1$  and  $\hat{W}_2$  as shown in Fig. 18.

### 3) VOLTAGE TRACKING PERFORMANCE WITH INTEGRAL SMC CONTROLLER

Fig. 15c shows the output voltage regulations with respect to the variations using SMC control method. From the results



**FIGURE 18.** Online weight tuning of AANN-PI under variable load  
a)  $\hat{w}_1$  b)  $\hat{w}_2$ .

it is evident that the SMC Control method is robust to the source variations but the tracking signal contains high frequency oscillations. Due to the high frequency oscillations, over shoot of 0.7V and undershoots of  $-0.7V$  are observed in the tracking signal. Fig. 16c shows the phase shift response measured and it shows high frequency chattering. Fig. 16a shows the measured results through oscilloscope

## VI. CONCLUSION

This paper proposed novel adaptive artificial neural network based PI controller for the isolated bidirectional dual active bridge converter. At first, the mathematical model of the dual active bridge converter is approached, and the state space model is derived. Based on the state space model, the nonlinear controller for output voltage of DAB converter is derived. Then, another controller is derived which is based on artificial neural networks and is integrated with PI controller. All the derived controllers are extensively evaluated and tested on 150W prototype of DAB. The controllers are tested for three different types of variations, and their response of convergence to the reference is examined. Based on these tests, the novel artificial neural networks based PI controller gives better performance as compared to the others in regards of overshoot; undershoot, rise time and settling time. Furthermore, from the obtained results it is cleared that proposed controller shows creditable response for all types of variations and is robust to parameters mismatch.

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