Grid fault ride-through of an LCC-VSC tandem converter, used as a grid interface for a multi-MW wind turbine.

M A Parker*, S J Finney †

*Department of Electrical and Electronic Engineering, University of Strathclyde
204 George Street, Glasgow, G1 1XW, UK, max.parker@strath.ac.uk
†University of Strathclyde, UK, stephen.finney@strath.ac.uk

Keywords: Wind Turbine, Tandem Converter, Grid Fault, Power Electronics

Abstract
A tandem converter, consisting of a line-commutated converter in parallel with a voltage source active filter, is proposed as a grid interface for a 10 and 20MW superconducting wind turbine generator. A commutation assistance scheme is introduced which allows the active filter to regulate the commutation of the LCC thyristors. This can allow ride through of grid faults by enabling the LCC thyristors to continue commutating with reduced grid voltage, which enables the use of tandem converters as a grid interface for renewable energy sources. The commutation assistance scheme is verified using simulation.

1 Introduction
Current-source converters have been proposed for wind energy applications [1], and one study has shown that costs could be considerably lower than voltage-source converters of the same rating [2]. However the load-commutated converters under consideration can have trouble coping with grid codes in terms of harmonics and grid fault ride-through. These problems can be solved with the addition of voltage-source active filters, making a tandem converter [3-5]. In a tandem converter, the current-source converter handles the main power flow with low cost and losses, while the active filter ensures a smooth current waveform. In this case, the active filter rating is about 25% that of the overall system rating.

A tandem converter, consisting of a load-commutated converter (LCC) in parallel with a voltage-source active filter based on a cascaded multilevel inverter, has been proposed as a grid interface for a large superconducting wind turbine generator [6]. Superconducting generators, using superconductors for the field windings, could allow the potentially higher reliability of direct-drive generators [7,8], but without the associated problem of high structural mass [9]. This is possible due to the high airgap flux density produced by the superconducting field windings [10,11]. Superconducting generators are under investigation by several companies including General Electric [12], Converteam (now part of GE) [13], ASMC and Siemens, as well as the EU-funded INNWIND project.

Superconducting generators have several features which present challenges to the design of the power electronic converter:

- A slotless design is usually employed for the rotor and stator, giving a synchronous reactance of around 0.1-0.2 per-unit. This can lead to high switching ripple as well as a high short circuit current.
- Harmonic distortion in the armature currents can lead to AC losses in the superconducting field coils, which must be removed by the cryogenic cooling system, requiring significantly higher powers than the losses themselves.
- To limit the leakage flux in the field windings, the generators have a low pole pitch, leading to a low rated AC frequency, as low as 2Hz.

In the INNWIND project, generator and converter designs were investigated for 10- and 20MW offshore wind turbines. While the conventional 3-level NPC converter was considered adequate for the application, and is proposed by GE for their generator design [12], the tandem converter design shown in Figure 1 was found to have several advantages. As well as potential cost and efficiency benefits, the use of a cascaded multilevel VSC and multiple series thyristors allows for tolerance of switching device faults [14,15], reducing the chances of a generator short circuit and improving reliability compared with the NPC converter.

![Figure 1: Tandem converter structure.](image)

The tolerance of device faults could also be achieved using a modular multilevel converter (MMC) [16], but the low generator frequency means that the DC-link capacitance requirement is high. The filter in the tandem converter, operating only on the current harmonics and rated at a quarter...
of the overall power, has been found to have a significantly lower capacitance requirement [6].

Previously published research on the proposed generator and converter was concerned with the optimisation of the capacitance of the generator-side filter, as well as control of the generator side of the converter [6]. This paper aims to address the grid-side control of the converter and active filter, although overall control will also be discussed.

A significant issue with the use of LCCs for grid interfacing of generators is that grid fault ride-through is difficult, as the drop in grid voltage during the fault can cause the thyristor commutation to fail. Existing literature on tandem converters mainly concerns their use as a motor drive, so they do not consider this problem [3-5]. The solution proposed in this paper is to control the grid-side active filter to support the natural thyristor commutation. A further problem, relating to the superconducting generator is the extremely low generator frequency. During a significant grid voltage dip at higher generator speeds, the firing angle of the rectifier thyristors must be increased as the DC voltage has dropped, but the change cannot occur until after a zero-crossing of the generator current. The low generator frequency means that a significant overcurrent could arise in this time unless a very high DC-link inductance is used. To prevent any overcurrent, the generator-side active filter can be controlled to force-commutate the rectifier thyristors, switching them instantly.

The structure of this paper is as follows: the parameters and design of the generator and converter will first be given. Controller design will be described, split into the design for normal operation and design for grid fault ride-through, with the latter containing details of the commutation assistance scheme. Simulation results will be presented, and finally conclusions given.

## 2 Generator and Converter Parameters

The main parameters for the generator and converter are given in Table 1. Both the generator- and grid-side filters use four series modules per phase, with the module capacitance optimised according to [6]. The grid filter switching frequency is set to give a current ripple of 5%, with the 0.1 p.u. grid transformer leakage inductance acting as the filter inductance. The chosen generator filter switching frequency gives a generator current ripple of less than 1%, but a lower frequency would lower the possible controller bandwidth as current sampling is synchronised to a multiple of the PWM frequency. A grid-side voltage of 3,500V is used in order to allow the firing angle of the rectifier to be kept at zero during normal operation, while keeping the inverter firing angle below 170° at rated power, in order to maintain controller stability.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power</td>
<td>10MW</td>
</tr>
<tr>
<td>Generator Rated Voltage</td>
<td>3,300V</td>
</tr>
<tr>
<td>Generator Inductance</td>
<td>21.6mH</td>
</tr>
<tr>
<td>Generator Rated Frequency</td>
<td>2.5Hz</td>
</tr>
</tbody>
</table>

Table 1: Generator and converter parameters

## 3 Controller Design

Control of the converter can be split into three aspects. First is the general control of the active filter in order to achieve sinusoidal generator and grid currents, and this section will also include a general discussion of the filter operation. Secondly is the overall control of the converter, in order to achieve a given generator current demand, and regulate the DC-link current of the main converter and DC-link voltages of the filters. Finally grid fault ride-through control is covered, which is based around using the active filters for commutation assistance. The first two sections are covered briefly, as they have already been described in [6].

### 3.1 Filter Operation and Control

A single line diagram of the converter inverter side along with the current controller is shown in Figure 2. The inverter will produce a square wave current $i_c$, with magnitude and phase dependant on the main converter DC current $I_{DC}$ and firing angle $\alpha$. The grid current, $i_g$, must be sinusoidal, and is the sum of the inverter and filter currents $i_c$ and $i_f$. Typical current waveforms are shown in Figure 3.

![Figure 2: Single line diagram of converter inverter side and controller.](image)

The complete converter is coupled to the grid via a coupling inductance $L_g$, and the voltage waveform produced by the multilevel active filter is smooth enough that the grid

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Grid Voltage</td>
<td>3,500V</td>
</tr>
<tr>
<td>Filter Series Modules</td>
<td>4</td>
</tr>
<tr>
<td>DC-Link Inductance</td>
<td>23mH</td>
</tr>
<tr>
<td>Generator Filter Switch Frequency</td>
<td>50Hz</td>
</tr>
<tr>
<td>Grid Filter Switch Frequency</td>
<td>200Hz</td>
</tr>
<tr>
<td>Maximum Filter Module DC Voltage</td>
<td>1,100V</td>
</tr>
<tr>
<td>Generator Filter Module DC Capacitance</td>
<td>36.6mF</td>
</tr>
<tr>
<td>Grid Filter Module DC Capacitance</td>
<td>5.7mF</td>
</tr>
</tbody>
</table>

Table 1: Generator and converter parameters
Transformer leakage inductance provides sufficient filtering. The filter is coupled to the inverter via a coupling inductance \(L_f\); the inverter thyristors commutate via the active filter, and this inductance is necessary to limit the rate of change of current during commutation. This will be discussed further in Section 3.3.

Grid current is controlled using the active filter, based on d- and q- axis current demands, \(i_{d,q}^*\). Conventionally for VSCs, a vector current controller based in the rotating reference frame is used. This is less suitable in this case, due to the discontinuities which occur around thyristor commutation. Instead, a feedforward voltage is calculated in the rotating reference frame, then used to calculate voltages \(v'_{abc}\). A proportional controller is used to regulate the current on a phase by phase basis, and mainly operates during thyristor commutation and transient changes in current demand. A similar system has successfully been used in an active rectifier scheme, in which similar discontinuities occur around the current zero crossings [15].

In the overall control scheme, described in Section 3.2, the firing angle of the inverter is used to regulate \(i_{DC}\), depending on the generator current demand. For an ideal thyristor inverter, the RMS AC current is given by Equation (1), and the power factor given by Equation (2)[17]. These can be used to calculate a feedforward value for the d-axis current demand \(i_d^*\), given by Equation (3). If this value is used, then the inverter and grid power flows will be balanced, and the DC voltages of the filter will be steady. As the converter is not ideal, the filter DC voltages must be actively regulated using a PI controller to set \(i_d^*\), using the value from Equation (3) as a feedforward term to improve response speed. In addition, voltage balance between phase strings in the active filter must be regulated, as well as balance between modules in each string. These are covered in more detail in [m] for the rectifier side, and will not be covered here.

\[
P F = \frac{3}{\pi} \cos \alpha \tag{2}
\]

\[
i_d^* = \frac{\sqrt{5}}{\pi} i_{DC} \cos \alpha \tag{3}
\]

### 3.2 Overall Control

In a similar manner to the grid filter, the generator filter is controlled to set the generator current and hence the generator torque. In this case, the generator current demand is set directly, and the generator filter DC-link voltage is regulated through the main converter DC-link current demand \(i_{DC}\). The DC-link current itself is controlled through the inverter firing angle \(\alpha\). In normal operation, the rectifier is operated with a firing angle of zero, with the same functionality as a passive rectifier. During grid voltage dips, \(\alpha\) is held at 170° and \(i_{DC}\) regulated using the rectifier firing angle.

### 3.3 Grid Fault Ride-Through

This paper is mainly concerned with the commutation assistance scheme, to allow inverter commutation during grid voltage dips and also to force commutate the rectifier, which are the basic requirements for grid fault ride-through of a tandem converter. A comprehensive operating strategy for operation during grid faults is incomplete at this stage, but some issues will be discussed in the conclusion.

Commutation of a grid connected thyristor-based inverter is shown in Figure 5, which represents a commutation between devices 1 and 3. In this diagram the grid voltage is \(v_{a,b,c}\), a current of \(i_{DC}\) flows, which can be assumed to be constant, and the thyristors commutate through an inductance \(L\). For commutation to occur when thyristor 3 is triggered, \(v_b\) must be greater than \(v_a\), with the current slope given by Equation (4).

\[
i_{rms} = \frac{\sqrt{2}}{\sqrt{3}} i_{DC} \tag{1}
\]
\[
\frac{di}{dt} = \frac{1}{2L}(v_b - v_a)
\]  

At time \(t_1\), representing a firing angle of \(180^\circ - \alpha\), thyristor 3 is fired, with the commutation completing at time \(t_2\), before \(v_b\) and \(v_a\) cross at time \(t_3\). In order for commutation to be successful, \(t_3 - t_2 > t_q\), where \(t_q\) is the turn-off time of the thyristor and is around 1.5ms for the devices under consideration.

In a conventional grid connected LCC, \(L\) is generally large as it consists of the grid coupling inductance as well as the inductance of the grid itself. In addition, \(\frac{di}{dt}\) must be limited during normal operation, to prevent damage to the thyristors. Because of this, during a grid fault when much lower grid voltages are present, \(\frac{di}{dt}\) may be too low to allow the thyristors to commutate in time.

If an active filter is present, then the filter inductance \(L_f\) will be much lower than the grid inductance \(L_g\), and the thyristors will commutate through the filter. While the filter voltages will generally follow the grid voltages, they can be temporarily changed to assist commutation. If \(L_f \ll L_g\), the impact on the grid current will be minimised. \(L_f\) determines \(\frac{di}{dt}\) during thyristor commutation, which must be kept within the limits of the devices, and this is achieved by limiting the commutation voltage \(v_{com}\) during the commutation interval.

The operation of the commutation assistance scheme is shown in Figure 6(a) for normal operation. Voltages are represented as continuous slopes, but in reality they will be multilevel PWM switched voltages. At time \(t_0\), before the start of commutation, voltages \(v_a\) and \(v_b\) are ramped to an identical value, to allow the thyristors to switch at zero voltage. At time \(t_1\) the voltages are ramped together as before, and thyristor 3 is fired at \(t_2\) and the voltage difference increased to the commutation voltage. After commutation, the commutation voltage must still be held for time \(t_q\) to prevent the commutation reversing, after which the voltages are returned to their normal values.

Operation with a reduced grid voltage is shown in Figure 6(b). Here \(v_{com}\) is greater than would normally be achieved, allowing commutation within the time limit.

Forced commutation is also possible, when \(v_a > v_b\), and this is shown in Figure 7. At \(t_0\) the voltages are ramped together as before, and thyristor 3 fired at \(t_1\) and the voltage difference increased to the commutation voltage. After commutation, the commutation voltage must still be held for time \(t_q\) to prevent the commutation reversing, after which the voltages are returned to their normal values.

![Figure 7: Forced commutation operation.](image)

**4 Simulation Results**

Converter operation was simulated using Simulink and SimPowerSystems. Normal operation of the inverter is shown in Figure 8, in which the operation of the commutation assistance scheme can be seen in the filter voltage around the thyristor switching events.

![Figure 8: Inverter waveforms.](image)

Some distortion can be seen in the grid current around the switching events even though the filter coupling inductance is a factor of 10 smaller than the grid coupling inductance, which is undesirable. This distortion could be reduced with a smaller filter inductance, allowing easier commutation, but
this would increase the $\frac{di}{dt}$ during commutation, towards the thyristor’s limits. Alternatively a higher grid coupling inductance could be used, but this would increase cost and weight.

The commutation action is shown in more detail in Figure 9, which shows the two filter voltages brought together at the thyristor switching instance, then set to a limited difference to allow commutation to occur. At this stage, a gentle ramp in filter voltage has not been implemented, and some technical issues mean that the thyristor trigger pulse occurs at the same time as the voltages are brought together rather than slightly later.

Figure 9: Detailed commutation action.

Response to a voltage dip is shown in Figure 10. For simplicity of control, the grid voltage is reduced to almost zero at 0.5s, which would ordinarily allow the turbine to disconnect under most grid codes. This is done in order to avoid having to maintain control over the DC-link current: the rectifier is simply forced to a shoot-through state, and the current slowly coasts down, while the generator current is reduced to zero by the active filter. Force-commutation of the rectifier is demonstrated, as is commutation assistance of the inverter during the grid voltage dip, and the converter continues to supply rated current.

Figure 10: Response to a grid voltage dip.

The grid fault strategy is currently incomplete, as several issues remain:

- A dip down to almost zero volts was simulated at rated turbine power, allowing the rectifier to be disconnected. In reality, the voltage dips will be less severe, and the rectifier will continue operating at an increased firing angle, which may cause severe DC current ripple.
- A severe voltage dip will result in a rapid reduction in power export capability, and even reducing the generator current significantly will cause a large amount of power to be absorbed by the generator-side filter, raising the capacitor voltages. It may be necessary to add dump resistors to the generator terminals using an additional thyristor bridge, or to the generator filter.
- Simulation of fault ride-through was carried out at rated wind speed. At low wind speeds, the DC current will be significantly lower, but the converter will be expected to supply rated reactive current during a fault. As there will be a delay before the DC current can be increased, the reactive current may need to be supplied by the grid-side active filter, considerably increasing the rating and cost.

Additionally, the simulations were carried out using a relatively simple generic thyristor model, and commutation performance may differ in a real life situation. Nevertheless,
this work shows that commutation assistance for the main LCC of a tandem converter is possible, and tandem converters could be a viable choice as a grid interface for wind energy and other distributed generating sources.

Acknowledgements

The research leading to these results has received funding from the European Community’s Seventh Framework Programme FP7-ENERGY-2012-1-2STAGE under grant agreement No. 308974 (INNWIND.EU), and the EPSRC’s ‘Underpinning Power Electronics 2012: Converters Theme’ research programme (EP/K035304/1).

References


