A Transition Arm Modular Multilevel Universal Pulse-Waveform Generator for Electroporation Applications

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Abstract—High voltage (HV) pulses are used in electroporation to subject pulsed electric field (PEF) onto a sample under treatment. Pulse-waveform shape, voltage magnitude, pulse duration, and pulse repetition rate are the basic controllable variables required for particular PEF application. In practice, a custom-made pulse generator is dedicated for each PEF application with limited flexibility in changing these variables. In this paper, a universal pulse-waveform generator (UPG) is proposed, where the controller software-algorithm can manipulate a basic generated multilevel pulse-waveform to emulate many different PEF pulse-waveforms. The commonly used PEF HV pulse-waveforms can be generated as bipolar or monopolar with controllable pulse durations, repetition times, and voltage magnitudes. The UPG has the ability to generate multilevel pulses that have controllable $dv/dt$ which allow reduction of the electromagnetic interference (EMI) generated by the converter. The UPG topology is based on half-bridge modular multilevel converter (HB-MMC) cells forming two transition arms in conjunction with two bi-state arms, together creating an H-bridge. The HB-MMC cell-capacitors provide a controllable energy source which charge from the dc input supply and discharge across the load, while the two bi-state arms allow charging the HB-MMC cell-capacitors. Hence, the UPG topology offers modularity, redundancy, and scalability. The HB-MMC individual cell-capacitance is low and the cell-voltages are balanced by employing the sorting and rotating algorithm used in conventional HB-MMC topologies for HVDC transmission applications. The viability of the proposed UPG converter is validated by MATLAB/Simulink simulation and scaled-down experimentation.

Index terms—Electroporation, high-voltage, modular multilevel converters (MMC), pulsed electric field, pulse-waveform generator, transition arm, series connected IGBTs.

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I. INTRODUCTION

ELECTROPORATION is the process where a biological cell-membrane is exposed to a specific pulsed electric field (PEF) [1]. Depending on the biological cell characteristics, a suitable PEF can be applied for electroporation [2]. Typically, there are two types of electroporation, namely: reversible and irreversible. In reversible electroporation, the cell-membrane survives after the electroporation process. This is required, for example, when inserting protein cells in the cell-membrane [3]. However, death of the microorganism’s cell is associated with irreversible electroporation [3]. Water treatment, air pollution control, and food sterilization are examples where irreversible electroporation is exploited for either decontamination or sterilization from bacteria and harmful microorganisms using PEF [4]-[7].

Generating the electroporation pulses is primarily controlled by the applied high voltage (HV) across the cell-membrane, pulse-waveform shape, and pulse application time. Therefore, there is a wide range of pulses, where a specific pulse suites a specific application. Accordingly, the voltage magnitude can be in the kilo-volt range (1-100 kV) while the pulse duration ranges between nanoseconds and milliseconds [8]. Generally, the higher the voltage, the shorter the pulse duration.

Among the wide range of possible HV pulse-waveforms for electroporation; rectangular, exponential, and combined narrow and wide pulse duration pulses are commonly used, as shown in Fig. 1 [8]. The HV pulses can be either monopolar or bipolar as shown in Fig. 1. The rectangular pulses, Figs. 1a and 1d, and the exponential pulses, Figs. 1b and 1e, are usually applied in water and air disinfection applications [9]. The pulses of combined narrow and wide pulse durations, shown in Figs. 1c and 1f, are recently proposed in [1], [5] and [10] and are preferred in safe food sterilization, without altering the food nutrition values and freshness quality [11]-[12].

Marx generators, pulse forming networks, and Blumlein lines are commonly used classical generators to generate electroporation HV pulses [13]-[15]. However, with ongoing HV semi-conductor switch development (in silicon and silicon carbide), mainly increased voltage rating and high switching frequency capability, it is possible to generate the necessary HV pulses via power-electronics based converters [16].
Exponential

Bi-state

Arm 1

Arm 2

Arm 3

Arm 4

Cell

Cell

Cell

Cell

Monopolar pulses

Monopolar pulses

Rectangular

Combined wide and narrow

Fig. 1. Commonly used pulse-waveforms in electroporation.

In a solid-state Marx generator, as an example, the semiconductor switches operate to control the charging and discharging of its capacitors such that they charge in parallel then discharge in series across the load [17]-[19]. The modular buck-boost converters in [20] generate HV pulses with proper turn ON/OFF operation of the semiconductor switches. Half-bridge modular multilevel converter (HB-MMC) cells are used in [21] to generate HV pulses from a low-voltage supply by charging the cell-capacitors sequentially, which are then discharged in series across the load. Modified HB-MMC cells are used in [22]-[23], as one phase-leg of the three-phase HB-MMC topologies used for HVDC application, to generate HV pulses from a HV dc supply. A series diode between adjacent cells modification affords sensorless operation of the MMC cell-voltages, when employing a specific ON/OFF switching sequence.

Neither classical nor modern semiconductor based pulse generators can generate different pulse-waveforms when utilizing one converter topology. Therefore, usually, there is a specific pulse generator for every associated PEF application, to generate its required pulses. Also, for a given pulse-waveform, some generators can only generate either monopolar or bipolar polarity pulses [24]. Series-connected HV switches for chopping the HV dc-voltage to form the pulses, adopted in [18], [19] and [25] for example, are evident. Similarly, in [21] HV series-connected diodes are required to bypass the load during cell-capacitor charging.

This paper proposes a universal HV pulse-generator (UPG) fed from HV dc-supply, \( V_s \), where the UPG is based on series connected HB-MMC cells forming two transition arms and two bi-state arms, together forming an H-bridge. The transition arm cell-capacitors provide a controllable energy source which allow multilevel voltage transitions. The bi-state arms, facilitate the charging of the HB-MMC cell-capacitors in the transition arms from \( V_s \) after discharging across the load. The transition arm concept was introduced in [26] for dual active bridge (DAB) application in HVDC transmission, in which the transition arm allow multilevel voltage transition between positive and negative voltage rails creating a quasi two-level voltage waveform with reduced \( dv/dt \) in order to reduce the voltage stresses on the DAB ac transformer. However, in this paper the two transition arms are utilized to build bipolar multilevel pulse waveforms such that one arm allow multilevel voltage transition between 0 and \( +V_s \) while the multilevel voltage transition in the other arm is between 0 and \( -V_s \).

Without changing any physical connections, by only changing the controller software algorithm, the UPG is able to generate a multilevel pulse waveform that has controlled \( dv/dt \) which allow reduction of the electromagnetic interference (EMI) generated by the converter [27]. Such a feature allows the generation of the basic HV pulses namely: rectangular, exponential as well as combined narrow and wide pulse-waveforms. Also, not only can bipolar or monopolar pulses be generated, but pulse-waveforms with special characteristics are also possible. For example, the proposed topology facilitates changing the duration of a particular polarity or merging both polarities null durations. Therefore, the UPG can be used in both reversible and irreversible electroporation applications.

Utilizing the HB-MMC cells and the bi-state arms in the UPG affords output flexibility by software control, along with hardware modularity, scalability, and redundancy. Each UPG HB-MMC cell-capacitance is low, which dramatically reduces converter cost, footprint and weight. Individual cell-voltages are balanced with the conventional sorting algorithm used in HB-MMC topologies for HVDC transmission applications, where cell-capacitors are continuously rotated and sequenced based on measurement of their individual voltage [28]-[29]. Although, each bi-state arm is formed by a series connection of IGBTs, the IGBTs are turned ON/OFF only when the voltage across the arm is zero, ZVS (or, near to zero). Transitional voltage sharing occurs with zero IGBT current, thus voltage sharing normally associated with hard switched series connected semiconductors, is not an issue.

The proposed UPG converter is introduced in section II and its operation principle outlined in section III. Its analysis and parameters selection are given in section IV, simulation and experimental results are present in sections V and VI respectively, and the proposed UPG topology variations and limitations are discussed in VII.

Fig. 2. The proposed UPG converter topology.
II. UPG CONVERTER TOPOLOGY

The proposed UPG converter topology is shown in Fig. 2. It consists of four arms forming an H-bridge, the two upper arms, Arm1 and Arm2, are the transition arms formed of N series connected HB-MMC cells while the two lower arms, Arm3 and Arm4, are the bi-state arms formed of series connected IGBT switches. Each transition arm has a small arm series inductor \( L_a \) to suppress the inrush current between cell-capsitors during their insertion process and to allow cell capacitor charging when connected to the dc input supply \( V_s \). Any HB-MMC cell of the transition arms has a capacitor \( C_c \) in series with an auxiliary IGBT switch/diode \( T_m \) and both are paralleled to a main IGBT switch/diode \( T_m \), as shown in Fig. 2. Each cell can operate in any of three switch states; bypass, insertion and idle, as shown in Fig. 2. The bypass state (when the main IGBT is ON and the auxiliary IGBT is OFF) implies that the cell is a short circuit and the voltage across the cell terminal \( V_{AB} \) is near zero. The insertion state (when the main IGBT is OFF and the auxiliary IGBT is ON) connects the cell capacitor to \( V_{AB} \), hence, the cell-voltage is \( V_{AB} = V_s \). The idle state (when both the main and auxiliary IGBTs are OFF) introduces an open circuit across the cell terminal at steady-state, thus, hindering the current flow through the arm from the dc-link. Therefore, the two transition arms have tri-state modes. Since the two lower arms are formed from series connected IGBTs, they can operate only in bi-state modes (either turned ON or OFF).

For proper UPG converter operation, each arm must be able to withstand the dc-link voltage \( V_s \). Consequently, the voltage of the HB-MMC cell-capsitors in the transition arm should be balanced and each cell-voltage fluctuates around \( V_s/N \). The MMC sorting and rotating technique in [28] is adopted. Therefore, each cell-voltage is continuously measured, compared with other cell-voltages in that corresponding arm, and the capacitor voltages are sorted. Accordingly, the highest voltage capacitor among the available capacitors is inserted first at each voltage level during the transition from \( 0 \) to \( \pm V_s \), while the lowest cell-voltage is bypassed first during the transition from \( \pm V_s \) to \( 0 \).

The UPG basic multilevel pulse-waveform \( V_p \) is shown in Fig. 3, and can be defined by four sequential intervals: positive pulse, positive null, negative pulse, and negative null, respectively. Controlling the five parameters defining this basic waveform allows the generation of any desired pulse-waveform, like the waveforms in Fig. 1. The five parameters are: repetition time \( T_s \), total pulse time \( t_p \), pulse plateau time \( t_m \), cell-voltage step \( V_s/N \), and step voltage-level applied time \( t_x \). Accordingly, there are \( N + 1 \) voltage levels from the zero-voltage-level to the peak pulse voltage \( V_s \) (neglecting IGBT ON resistances, diode voltage drops and arm inductor resistance). The positive and the positive null intervals in the UPG basic waveform (regions 1 and 2 in Fig. 3, respectively) can be expressed mathematically as in (1):

\[
\begin{align*}
 v_p &= \begin{cases} 
 0 \quad &0 \leq t < Nt_x \\
 \sum_{n=1}^{N} \left( \frac{nV_s}{N} \right) \quad &Nt_x \leq t < (Nt_x + t_m) \\
 \left( V_s - \frac{nV_s}{N} \right) \quad &(Nt_x + t_m) \leq t < t_p \\
 0 \quad &t_p \leq t < \frac{1}{2}T_s
\end{cases}
\end{align*}
\]

Mathematically, region 3 is the negative equivalent of region 1, while region 4 is similar to region 2. Accordingly, Table I defines the emulated pulse-waveforms of Fig. 1 generated by the UPG from its basic pulse-waveform controlling attributes.

<table>
<thead>
<tr>
<th>Waveform</th>
<th>Controlling attributes</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rectangular Pulse</td>
<td>By setting ( t_p = t_m ) and ( t_x = 0 ), accordingly all the corresponding arm cells will operate simultaneously.</td>
</tr>
<tr>
<td>Exponential Pulse</td>
<td>By inserting all corresponding cell-capsitors for duration ( t_m ), then bypassing them one by one gradually to reduce the voltage to zero in ( Nt_x ), hence ( t_p = t_m + Nt_x ).</td>
</tr>
<tr>
<td>Combined Narrow and Wide Pulse Durations</td>
<td>By setting ( t_p ) as the wide pulses duration with an amplitude of ( V_c ), and the narrow pulses of magnitude ( \left( V_c - \frac{mV_s}{N} \right) ) and duration ( t_x ). Where ( m ) is the number of bypassed cells in the corresponding arm.</td>
</tr>
</tbody>
</table>

III. UPG OPERATION PRINCIPLE

The UPG generated pulse-waveforms, are normally bipolar, where the HB-MMC cells in Arm2 are responsible of generating the positive pulse-duration, while the HB-MMC cells in Arm1 are responsible for generating the negative-pulse duration. Disabling a specific transition arm will lead to generating monopolar pulse-waveforms with the desired polarity.

As discussed, there are four intervals for a bipolar pulse-waveform generation cycle. The operation for each interval is shown in Table II and can be explained as: during the positive pulse generation interval, the two bi-state arms are OFF, transition Arm1 cells are bypassed and transition Arm2 cells are inserted.
Consequently, the equivalent series connected capacitor $C_{c}/n$ is inserted across the load producing a voltage of $+nV_{c}/N$, where $n$ is the number of the inserted cells. Similarly, for negative pulse generation, again the two bi-state arms are OFF, transition Arm2 cells are bypassed and transition Arm1 cells are inserted. Hence, the series connected capacitor $C_{c}/n$ is inserted across the load producing voltage $-nV_{c}/N$. During the null intervals, Arm2 or Arm1, for positive or negative pulses respectively, cell-capacitors are charged from the dc-link supply, while the load voltage is nullified. Thus, the equivalent inserted capacitor $C_{c}/N$ charges to $V_{c}$. Turning ON/OFF the bi-state arms is assured soft-switching operation, ZVS (that is, the series-connected switches are switched only when the voltage across the arm is near zero). After the positive/negative pulse interval, charging of Arm2/Arm1 is the next interval which requires turning ON the bi-state Arm4/Arm3.

Thus, during charging of one of the transition arms, the other transition arm is idled, and the charging cell-capacitors are inserted. Therefore, the voltage across the corresponding bi-state arm remains close to zero, hence it can be turned ON allowing the input supply to charge the cells. During the charging interval, the charging transition arm voltage reaches the supply voltage $V_{c}$, hence, the voltage difference across the corresponding bi-state arm is zero, and therefore, the bi-state arm can be turned OFF, in a ZVS state.

Idling one transition arm during the null intervals (or, the charging interval of the other transition arms) will not prevent charging its capacitor-cells, through $T_{x}$ diode, when starting the converter. At steady-state, the idled arm cell-capacitor voltages are charged to the supply voltage $V_{c}$, and since they are not participating in the pulse generation, the arm voltage will not change, hence the idled arm can be viewed as an open circuit during charging of the other transition arm, as shown in Table II.

### IV. PARAMETERS SELECTION OF THE UPG

Since bipolar rectangular pulse-waveform represents the case where all the cell-capacitors in the transition arms (Arm1 and Arm2) are to be inserted, giving a step from the zero voltage-level to the $\pm V_{c}$ voltage-levels, its generation will be considered in the following analysis and cell-capacitor sizing. In this paper, HV pulses with pulse durations of microseconds or longer are considered, hence, the PEF load is modeled as a resistive load, $R$ [4] and [30].

![Fig. 4. Individual cell-capacitors of Arm1 and Arm2 charging and discharging sequence for generating a rectangular bipolar pulse.](image-url)
The UPG operation for each pulse polarity can be illustrated from an energy conversion perspective. Assume the individual cell-capacitors are pre-charged to \( V_s/N \). During the positive pulse interval some of the energy stored in Arm2 capacitors is transferred to the load during pulse generation time \( t_p \). Hence, Arm2 capacitors partially discharge and their voltage reduced accordingly. This energy reduction is compensated by recharging Arm2 individual cell-capacitors to \( V_s/N \) during the positive-null interval. These two processes are illustrated in Fig. 4. The same procedures occur during the negative-pulse interval through Arm1 cell-capacitors. Therefore, the energy is transferred from the input dc supply to the load in two stages, first the input dc supply charges the corresponding cell-capacitors then the charged cell-capacitors partially discharge into the load. The energy transferred to the load \( E_L \) during the pulse time is

\[
E_L = P_L t_p
\]

where \( P_L \) is the power consumed by the load \( R \) during pulse time \( t_p \) which can be expressed as,

\[
P_L = \frac{V_o^2}{R}
\]

where \( V_o \) is the load rms voltage which can be calculated from Fig. 4 as

\[
V_o = \sqrt{\frac{1}{T_S} \int_0^{T_S} v_o^2(t) \, dt}
\]

thus,

\[
V_o = \frac{2t_p}{T_s} V_p
\]

Neglecting IGBT voltage drops and the internal resistance of \( L_a \), then \( V_o \approx V_s \) hence, the load energy per pulse polarity is

\[
E_L = \frac{t_p^2 V_s^2}{T_s R}
\]

The energy transferred to the load is equal to the difference between the initial energy stored in the individual arm capacitors \( E_{cell}^i = \frac{\alpha C_c V_s^2}{N^2} \) and the energy remaining after the pulse \( E_{cell}^p = \frac{\beta V_s^2}{N^2} \). This energy can be estimated as

\[
\Delta E_{cell} = E_{cell}^i - E_{cell}^p
\]

hence,

\[
\Delta E_{cell} = \frac{\alpha C_c V_s^2}{N^2} - \frac{\beta V_s^2}{N^2}
\]

where \( \Delta E_{cell} \) is the energy difference in the individual cell-capacitor in the discharging arm, and the cell-voltage after discharge is expressed as a per unit, \( \beta \), in terms of the initial cell-voltage \( V_s/N \) as shown in Fig. 4. Thus the energy difference in the discharging transition arm of \( N \) series cells is

\[
\Delta E_{Arm} = N \times \Delta E_{cell} = \alpha C_c \frac{(1 - \beta^2) V_s^2}{N^2}
\]

where \( \Delta E_{Arm} \) is the energy difference in the arm corresponding to a specific pulse polarity. Neglecting semiconductor losses, then \( \Delta E_{Arm} = E_L \), hence the cell-capacitance is

\[
C_c = \frac{2t_p^2 N}{(1 - \beta^2)T_s R}
\]

Denoting \( \delta = t_p/T_s \), the pulse on-state duty ratio, (10) yields

\[
\alpha = \frac{2N\delta t_p}{(1 - \beta^2)\alpha}
\]

where \( \alpha > 1 \) is a safety factor to account for neglected losses. \( C_c \) can be tailored to produce an acceptable pulse droop for a given load and pulse characteristic (increase \( C_c \) to decrease the voltage droop). The cell-capacitor per unit voltage droop, \( (1 - \beta)^2 \), specifies the efficiency of the resonate recharging of the cell-capacitors. The resonant losses, related to the throughput power, are approximately the per unit droop, squared, viz. \( (1 - \beta)^2 \). For example, with 10% capacitor voltage droop, the recharging losses are 1% the average power of the pulse causing the droop. These losses are dissipated in the charging circuit resistance and the bi-state arm devices as on-state losses. These device losses are mitigated by the fact that the bi-state arm devices do not incur switching losses; their switching is at zero voltage, ZVS.

Consequently, the total average power \( P_{tot} \) associated with the applied HV pulses with repetition frequency \( f_s \), can be calculated as (12) for bipolar pulses, while the value is halved for monopolar pulses.

\[
P_{tot} = \frac{2t_p^2 V_s^2}{R f_s^2}
\]

The arm inductor \( L_a \) in series with the MMC cells in HVDC applications has two functions; suppressing the circulating current in the cells and limiting the dc short circuit current [29]. Although, the proposed UPG converter topology adopts the technology of an MMC-HVDC converter, the operating principle is different, and dc-link short circuit blocking is not applicable. But, small arm inductance is preferred in each transition arm (Arm1 and Arm2) such that the inrush current is suppressed when inserting the cells in series across the load during discharging or across the dc supply during charging. Additionally, resonance between \( L_a \) and the arm capacitors should be avoided [30]. Hence, the device switching frequency should be well away from the resonance frequency of the equivalent LC circuit to avoid exciting resonance currents. Therefore, based on the designed equivalent arm capacitance and repetition time, an estimation of the inductance of \( L_a \) is

\[
L_a < \frac{NT_s^2}{4\pi^2 C_c}
\]

V. SIMULATION RESULTS

The proposed UPG is assessed using MATLAB/Simulink simulations, with the parameters in Table III. The cell-capacitance \( C_c \) is calculated based on (11), while (13) is used to estimate the arm inductance \( L_a \). The number of the IGBT switches in bi-state arms, Arm3 and Arm4, is \( N = 10 \). The simulations assess the ability of the UPG to mimic the commonly used pulse-waveforms in PEF applications and the flexibility of the converter to control the generated pulse attributes via the controller software-algorithm, without any physical changes to the power topology or the load connection.
TABLE III
SIMULATION SPECIFICATION

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>DC input voltage $V_s$</td>
<td>10 kV</td>
</tr>
<tr>
<td>Number of cells/arm $N$</td>
<td>10</td>
</tr>
<tr>
<td>Repetition time $T_s$</td>
<td>200 μs</td>
</tr>
<tr>
<td>Arm inductance and resistance $L_a$, $R_a$</td>
<td>2 μH, 0.1 Ω</td>
</tr>
<tr>
<td>Arm resistance $R_a$</td>
<td>0.1 Ω</td>
</tr>
<tr>
<td>Load resistance $R$</td>
<td>1 kΩ</td>
</tr>
<tr>
<td>Cell-capacitance $C_c$</td>
<td>5 μF</td>
</tr>
<tr>
<td>Per unit remaining capacitor voltage $\beta$</td>
<td>0.95</td>
</tr>
<tr>
<td>Safety factor $\alpha$</td>
<td>2</td>
</tr>
</tbody>
</table>

With a repetition frequency of 5 kHz, the basic UPG multilevel pulse-waveform is shown in Fig. 5. Fig. 5a shows the generated voltage pulse with $t_x = 2 \, \mu s$, $t_m = 14 \, \mu s$ and $t_p = 50 \, \mu s$. The cell-voltages of Arm1 and Arm2 are shown in Fig. 5b. In Fig. 5b, Arm2 contributes to the positive pulse generation while Arm1 contributes to the negative pulse generation. The cell-capacitors charge to the 1 kV after their voltage decreases during discharge across the load. The voltage across and the current through the bi-state arm Arm4 during one cycle of pulse generation is shown in Fig. 6. In Fig. 6b when the voltage across Arm4 is near zero, the bi-state arm switches are turned ON allowing capacitor charging current to flow. After the cell-capacitors are charged, the current reaches zero and the voltage across the bi-state arm is zero, hence, the arm switches can be turned OFF. During negative pulse generation, the bi-state arm is maintained OFF and the voltage across it is $V_s$ as shown in Fig. 6a.

Fig. 7a shows the generation of positive monopolar multilevel pulse-waveform, while the negative counterpart is depicted in Fig. 7b. Since the two transition arms can operate independently, not only a certain polarity can be omitted, but it can be generated with different characteristics. For example in Fig. 8a, the positive pulse is generated with $t_x = 2 \, \mu s$, $t_m = 24 \, \mu s$ and $t_p = 60 \, \mu s$ while the negative pulse is generated with $t_x = 2 \, \mu s$, $t_m = 2 \, \mu s$ and $t_p = 38 \, \mu s$. Since the positive pulse duration is longer, the decrease in the cell voltages of Arm2 is more than those for Arm1, see Fig. 8b.
By setting $t_x = 0$ and $t_m = t_p = 10 \mu s$, in the controller software, a rectangular pulse can be generated. The simulation results of bipolar rectangular pulses are shown in Fig. 9a. Generating positive monopolar and combined null duration rectangular pulses is explored in Figs. 9b and 9c, respectively, with positive and negative pulse durations of 10μs.

The features of controlled $dv/dt$ in the basic generated multilevel pulse waveform and the conventional rectangular pulse waveform can be combined as shown in Figs 10a and 10b with repetition frequency of 10 kHz. The exploitable lower value of $t_x$ is limited by switching device turn ON and OFF delay times. In Fig. 10a the $t_x$ value is 0.1 μs (viable with SiC 1700V, 45mΩ MOSFETs, with 50ns turn ON and OFF delays) while $t_m$ is 5 μs, hence the total rise time is much shorter than the plateau time. If a faster rise time is required, grouping of the cells can be employed. As shown in Fig. 10b, to achieve a rise time of 0.3 μs the ten cells are voltage sorted into three groups, two groups of 3 cells and one group of 4 cells.

Employing the flexibility afforded by the generated basic multilevel pulse-waveform from the UPG allows generating both exponential as well as combined wide and narrow pulse-waveforms. Such waveforms can be generated with either bipolar or monopolar polarities. With $t_x = 2 \mu s$, $t_m = 10 \mu s$, Fig. 11a shows bipolar exponential pulses and Fig. 11b shows positive monopolar exponential pulses. Consequently, with $m = 5$ and $t_x = 10 \mu s$, bipolar combined wide and narrow pulses are shown in Fig. 12a and the negative monopolar version is shown in Fig. 12b.
**VI. EXPERIMENTAL RESULTS**

The scaled-down UPG power circuit from Fig. 2 uses IGBT switches (STGW30NC60WD) for the HB-MMC arms which have antiparallel diodes, while the bi-state arms use Infineon IGW60T120 IGBTs. The control algorithm is implemented in eZDSP, which is a digital signal processor (DSP) board based on the Texas Instruments TMS320F28335 DSP, to generate the required gating signals for the UPG switches in the four arms. The experimental specifications are given in Table IV, and the scaled-down experimental rig is shown in Fig. 13. The experimentally generated four-level basic multilevel pulse-waveform is shown in Fig. 14. The output voltage pulses are shown in Fig. 14a with $t_x = 20\,\mu s$, $t_m = 40\,\mu s$ and $t_p = 120\,\mu s$, whereas a cell-capacitor voltage from Arm1 and Arm2 are presented in Fig. 14b. Since the input supply voltage is 150V, the cell-capacitors are charged to 50V, then during the associated pulse time the corresponding transition arm capacitors discharge across the load reducing the cell-capacitors voltage to 44V. Fig. 15 shows the waveforms of voltage across and the current through the bi-state arm Arm4 during a complete cycle of the generated pulse. The waveforms reveal that the bi-state arm is turned ON/OFF only when the voltage across the arm is near-zero, ZVS. With $t_x = 20\,\mu s$, $t_m = 40\,\mu s$ and $t_p = 120\,\mu s$ negative plus positive monopolar four-level voltage pulses are shown in Figs. 16a and 16b, respectively.

**TABLE IV**

<table>
<thead>
<tr>
<th>EXPERIMENTAL SPECIFICATION</th>
<th>$V_s$</th>
<th>150V</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of cells/arm</td>
<td>$N$</td>
<td>3</td>
</tr>
<tr>
<td>Repetition frequency for bipolar pulses</td>
<td>$1/T_s$</td>
<td>1.8 kHz</td>
</tr>
<tr>
<td>Repetition frequency for monopolar pulses</td>
<td>$1/T_s$</td>
<td>3.6 kHz</td>
</tr>
<tr>
<td>Arm inductance and resistance</td>
<td>$L_a \cdot R_a$</td>
<td>1 mH, 0.2 $\Omega$</td>
</tr>
<tr>
<td>Load resistance</td>
<td>$R$</td>
<td>100$\Omega$</td>
</tr>
<tr>
<td>Cell-capacitance</td>
<td>$C_c$</td>
<td>15 $\mu F$</td>
</tr>
<tr>
<td>Per unit remaining capacitor voltage</td>
<td>$\beta$</td>
<td>0.9</td>
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<tr>
<td>Safety factor</td>
<td>$\alpha$</td>
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</tbody>
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Fig. 12. Simulation of UPG combined wide and narrow HV pulses with $m = 5$ and $t_x = 10\,\mu s$: (a) bipolar and (b) negative monopolar.

Fig. 13. Scaled-down experimental test rig.

Fig. 14. Experimental results for UPG four-level bipolar pulses with a 50V level: (a) output pulses and (b) a cell-capacitor voltage in the 2 transition arms.

Fig. 15. Voltage across and current through bi-state Arm4 during complete cycle of pulse generation via Arm2 cell-capacitors.
Generating rectangular pulses with different characteristics is explored in Fig. 17. Symmetrical rectangular bipolar pulses with $t_m = 20\mu s$ are shown in Fig. 17a, whereas a cell-capacitor voltage from Arm1 and Arm2 are presented in Fig. 17b. Positive monopolar pulses with $t_m = 40\mu s$ are shown in Fig. 17c, while combined null-durations pulses are illustrates in Fig. 17d.

Asymmetric rectangular pulse generation is shown in Figs. 18a and 18b. Fig. 18a shows a $40\mu s$ rectangular positive pulse followed by a $20\mu s$ negative pulse. An asymmetric combined null-durations rectangular pulse is depicted in Fig. 18b with pulse times of $20\mu s$ and $40\mu s$, positive and negative respectively.

Bipolar exponential pulses with $t_x = 20\ \mu s$ and $t_m = 40\ \mu s$ are shown in Fig. 19a and a cell-capacitor voltage in the 2 transition arms are shown in Fig. 19b. While the monopolar version is shown in Fig. 19c. The combined wide and narrow pulses with $m = 2$ and $t_x = 20\ \mu s$ are shown in Figs. 20a, 20b and 20c for bipolar pulses, its cell-capacitors voltage in the 2 transition arms, and its monopolar version of pulses, respectively.

Finally, to explore the validity of the concept in generating wide range of pulses, a bipolar rectangular pulse of duration of $4\mu s$ is generated with repetition rate of $10\ kHz$ and voltage of $0.6\ kV$ peak-peak as shown in Fig. 21.

It should be noted that, the series connected IGBTs in the bi-state arms, although in an OFF-state during rail-to-rail transitions, require shunt connected voltage sharing resistors and parallel capacitors to account for static leakage and output capacitance variation between series devices.
Fig. 18. Experimental results for UPG asymmetrical rectangular pulses: (a) bipolar with positive $t_m = 40\mu s$ and negative $t_m = 20\mu s$ and (b) combined null durations with positive $t_m = 20\mu s$ and negative $t_m = 40\mu s$.

Fig. 19. Experimental results for UPG exponential pulses with $t_x = 20\mu s$ and $t_m = 40 \mu s$: (a) bipolar (b) a cell-capacitor voltage in the 2 transition arms, and (c) monopolar.

Fig. 20. Experimental results for UPG combined wide and narrow pulses with $m = 2$ and $t_x = 20\mu s$: (a) bipolar (b) a cell-capacitor voltage in the 2 transition arms, and (c) monopolar.

Fig. 21. Experimental results for UPG bipolar rectangular pulses with $t_m = 4 \mu s$, repetition frequency of 10 kHz and voltage of 0.6 kV peak-peak.
VII. TOPOLOGY VARIATIONS AND LIMITATIONS

There are UPG topology variations, dependent on the application requirements.

Arm inductance: The arm inductor $L_a$ is small and may be accounted for by the inherent internal wiring and connection of the cells. Clamping the inductors with freewheel diodes is possible but the low voltage of the forward biased diode may result in an excessively long $L/R$ reset time constant, which will limit the upper operating frequency. The energy of remaining unclamped stray inductance, rings with the cell capacitors.

Elimination of bi-state arm diodes: MMC cell-capacitor recharging involves an $LCR$ oscillation, where if under damped, the current alternately reverses. The damping losses are independent of the circuit $Q$, thus critical damping incurs the lowest stresses, with an acceptable settling time. As such, without current reversal, the bi-state arm diodes across the IGBTs are redundant. To maximize power transfer, any (added and/or exiting) resistance should be in the bi-state arm. Both MMC series inductors can be moved to their associated series bi-state arm.

Bi-state arm switches: The switching properties of the bi-state arm devices can be much slower than those necessary for the transition arms (HB-MMC arms). Also, because their inter dc rail state transitions are with zero current, they can be higher voltage (consequently slower) rated devices, used near their voltage limit. Thus the number of bi-state arm series connected devices can be much less than $N$, the number MMC cells. The tradeoff of higher voltage devices is higher OFF-state leakage current, so the parallel connected static voltage sharing resistance is decreased.

The input supply voltage: The proposed UPG topology for generating the HV pulses can be supplied from a dc-dc boosting stage. Additionally, the use of a 12-pulse ac-dc thyristor half-controlled converter to supply the UPG offers soft start up and shut down.

Finally, the ability of the converter to generate high repetition rate pulses only depend on the speed of selected controller in executing the control software instructions, such that the total software execution time is less than the required pulse repetition time. For high repetition rates and/or short pulse durations, the utilisation of fast (short turn ON delays) semi-conductor switches is mandatory.

VIII. CONCLUSION

This paper presented a universal HV pulse-waveform generator, UPG, which emulates the dominate HV pulse-waveforms in PEF electroporation applications. The proposed UPG topology is based on transition arms of series connected HB-MMC cells, which has been utilized for quasi two-level voltage generation in DAB dc-dc converters for HVDC transmission applications. In the UPG, two transition arms are used for negative and positive voltage pulse generation along with two bi-state arms which allow charging of the transition arms capacitors. The proposed UPG generates multilevel pulses that has low $dv/dt$ which allow reduction of the electromagnetic interference (EMI) generated by the converter. The controller software-algorithm allows manipulation of the multilevel pulse-waveform characteristics to generate rectangular, exponential and combined narrow and wide pulses. The UPG can generate both bipolar and monopolar HV pulse-waveforms of micro-second pulse durations with controllable voltage magnitude, pulse duration and pulse repetition frequency characteristics. Therefore, the proposed topology provides flexibility via its controller software-algorithm, along with hardware modularity, scalability, and redundancy. The HB-MMC cell-capacitors in the transition arms provide a controllable energy source which charges from the dc input supply and discharges across the load. The bi-state arms allow charging of the HB-MMC cell-capacitors in the transition arms from the dc input supply. Unlike the conventional HB-MMC used in HVDC transmission applications, the UPG converter has reduced footprint, weight and cost by utilizing small cell-capacitances in the HB-MMC and employing bi-state arms. The cell-capacitor voltages are maintained around $V_d/N$ by measuring their individual voltages, continuously, and applying a sorting and rotating algorithm. Simulations and experimental results confirm the viability of the proposed UPG, which promotes it for PEF application.

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REFERENCES

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