

# The impact of MVDC upon conventional distance protection schemes in hybrid ac-dc distribution networks

*L. Hunter* \*, *C. Booth* \*, *A. Dysko* \*, *S. Finney* † *A. Junyent-Ferré* §

*\* University of Strathclyde, Glasgow, Scotland,*

*{lewis.hunter.100, campbell.d.booth, a.dysko}@strath.ac.uk*

*† Electronics and Electrical Engineering, The University of Edinburgh, Scotland, stephen.finney@ed.ac.uk*

*§ Department of Electrical and Electronic Engineering, Imperial College London, UK, adria.junyent-ferre@imperial.ac.uk*

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## Abstract

This paper looks at the protection implications of introducing fully controllable, embedded, medium voltage dc (MVDC) power electronic links into power distribution networks. Studies have indicated, protection notwithstanding, that embedded MVDC can be used to provide economically attractive, enhanced capacity and control of power flows.

Through a series of simulation studies, the impact on distance protection schemes (in terms of reach and response time) resulting from the introduction of a controlled MVDC link have been examined for symmetrical faults. The paper also considers under what conditions a dc side fault can be observed from the ac grid.

A series of recommendations for system integrators are made.

## 1 Introduction

As they evolve into Distribution System Operators (DSOs), Distribution Network Operators (DNOs) are under pressure to find innovative ways to accommodate rising demand and embedded generation using existing system assets. Several GB (Great Britain) regulator funded innovation projects in the UK have been tasked with determining the advantages and pitfalls of new approaches to increasing flexibility within distribution networks [1] [2].

Embedded medium voltage direct current (MVDC) links could give the future DSO the ability to load network sectors more dynamically to allow significant increases in power flows whilst not exceeding the firm capacity of grid supply point transformers [3] [4]. This idea is being demonstrated by SP Energy Networks through a network innovation competition (NIC) funded project supported by the GB grid regulator, OFGEM (Office for Gas and Electricity Markets) [5]. Large multinational engineering conglomerates are now offering MVDC solutions for both land and sea based applications [6] [7].

The protection required to safeguard controlled links from a dc side fault needs to be fast acting otherwise the power electronic devices are likely to be damaged to the point of replacement. This philosophy is somewhat contrary to traditional ac protection where trip signals may actually be delayed to allow more local circuit breakers to open first.

The approach developed and presented in this paper combines Simulink modelling for the power electronics and the 33 kV network with a distance protection relay modelling tool developed at the University of Strathclyde and verified by National Grid. The studies examine a section of real Scottish distribution network with protection settings derived from the local DNO's long term development statement.

Several fault scenarios have been analysed for different fault locations and operational modes of the controllable MVDC link. For local ac system side faults, converters respond with a fault ride through capability similar to that set out in ENTSO-E for a Type D Power Park [8] (covering connection of medium scale ( $\leq 30$  MW) embedded generation to the transmission system) as grid codes for distribution connected MVDC do not, as yet, exist. For dc side faults, a simplified converter protection strategy is employed.

The primary outcome of this work is to determine whether distance protection remains a valid approach for hybrid ac-dc distribution networks. In particular, the model is used to assess whether a suitably configured distance relay can provide sufficiently fast, backup protection should local ac circuit breakers at a dc link converter station fail.

Studies examine and quantify how the relay reach is affected by downstream MVDC links and how zone boundaries move depending upon the converter's operational mode.

## 2 Protection Requirements

The following subsections will discuss the protection requirements of 33 kV ac distance protection schemes in addition to the fault characteristics of MVDC power electronic converters.

## 2.1 Distance Protection Philosophy

The tripping time of a distance protection relay is determined by the electrical distance between the relay measurement point and the calculated fault zone. Distance relays rely upon the measurement of both voltage and current from which the fault impedance is derived using ohms law. A fault near the measurement point will result in a lower calculated impedance than a more remote fault. This principle allows operational zones, typically three, to be defined as presented in Figure 1 [9].

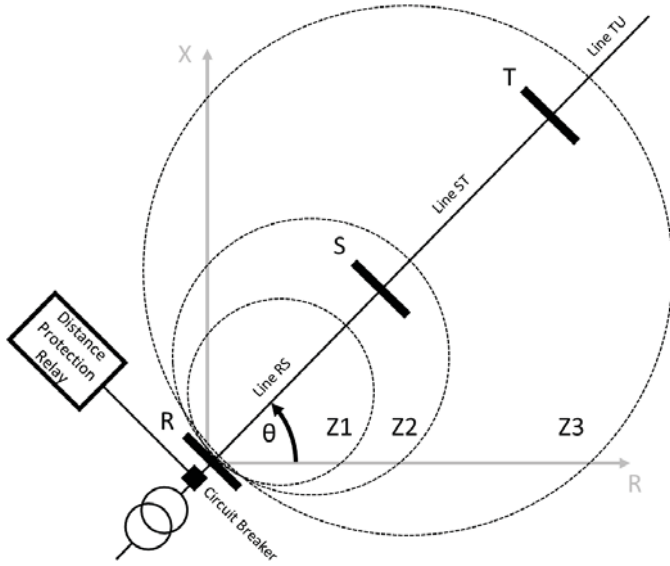


Figure 1: Distance protection zone diagram

- Zone 1 generally reaches to approximately 80 % of the first protected feeder (Line RS) to prevent overreach into Zone 2 which would be caused by errors in the transducers, relay and line parameters' estimation.
- Zone 2 is typically set to 120 % impedance of the protected line (120% of Line RS).
- Zone 3 typically reaches to 120% of the combined impedance of lines RS and ST.

Suggested distance protection operation times for a distribution network operator are presented in Table 1. These times are designed to allow discrimination thus allowing a more local circuit breaker to clear the fault first. This time delayed zone approach is designed to reduce the number of customer interruptions associated with a downstream fault.

Zone	Clearance time (s)
Z1	0.150
Z2	0.500
Z3	1.3

Table 1: Maximum clearance times for distance protection schemes as recommended by SSSEN [10]

Distance protection can distinguish between eleven possible fault types; AG, BG, CG, AB, BC, CA, ABG, BCG, CAG, ABC, ABCG where A, B and C are phase conductors and G is

ground. This paper will only consider symmetrical faults due to the early stage nature of this research.

## 2.2 Converter Protection Philosophy

While converters can provide a degree of support to ac side faults their ability to support a dc fault is extremely limited for most topologies. This limitation is primarily due to the freewheel diodes placed across IGBT devices creating a fault path similar to that displayed in Figure 2. The converter effectively becomes an uncontrolled rectifier with current only being limited by the ac side arm inductance [11].

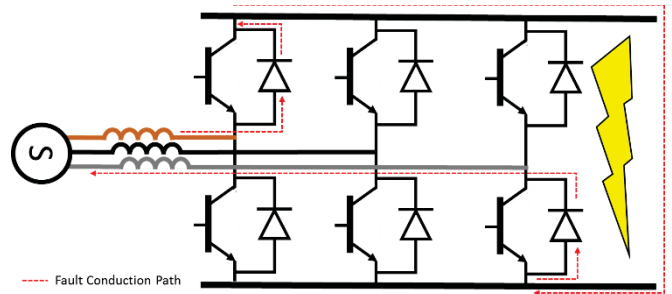


Figure 2: Converter fault path through freewheel diode

The modelled fault response for both ac and dc faults will be outlined.

### A. AC side fault strategy

At present, there are no obligations for a MVDC link to provide any fault ride through capability as there is no requirement in the GB distribution code imposed on DNOs for such a technology of a <50 MW rating [12].

For the purposes of this study a fault ride through (FRT) capability similar to that set out in ENTSO-E requirements for generators (RfG). A Type D Power Park will be considered (covering connection of medium scale ( $\leq 30$  MW generation) [13] [8]. The minimum and maximum responses for such a unit are outlined in Figure 3.

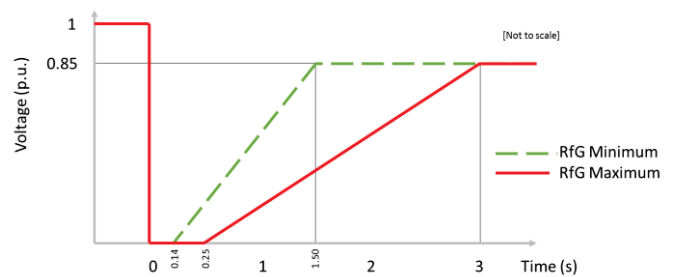


Figure 3: ENTSO-E fault ride through requirements for Type D Power Park [13]

This FRT characteristic of MVDC converters will generally only be capable of delivering fault current into faults on parallel circuits. This is due to the synchronising controller of the MVDC link being unable to lock for series faults as the voltage will be zero at the inverter's point of network coupling. The fault ride through characteristic determines the length of

time a generating unit should remain connected to the network for a measured terminal voltage.

It is assumed that the converter, where practical, will support network voltage during fault conditions with an infeed of current which is limited to the converter's rating. It is appreciated that other responses are possible.

#### B. DC side fault strategy (two/three level converters)

When a dc side fault occurs and is detected by the converter controller the first step taken by the protection system is to block commutation of the switching devices. This prevents the power electronic devices being subjected to high fault currents which would only be limited by converter arm impedances.

The anti-parallel diodes in Figure 2 will conduct once the dc side voltage has collapsed below the ac voltage envelope. Bypass devices, often high capacity thyristors, are commonly used to divert current allowing time for protection to operate.

The next action is to open ac side converter circuit breakers to isolate current infeed to the link. This needs to happen quickly to protect the parallel diodes from overheating and critically failing. Upon disconnection at both converter stations, earth switches on the dc sides can be closed via resistor banks to de-energise the link capacitance to a safe voltage.

Should either of these steps fail (converter controller failure, trip signal fail, maloperation of ac side circuit breaker), the converter could be left in a damaging uncontrolled rectifying state [11].

### 2.3 Compatibility

While the time scales involved in protection of ac and dc systems are of differing horizons, studies in this paper aim to;

- For ac faults, investigate whether the distance relay reach (or time) can be adversely affected by an MVDC link
- Determine what the 'seen' impedance of a dc side fault looks like from the ac grid
- Discuss if a suitably configured distance relay can accurately detect and discriminate for a dc side fault for backup protection of a MVDC link.

This paper more generally is to look at the performance of ac distance protection in networks with embedded MVDC.

## 3 Study Overview

This section describes the steps carried out to investigate the impact of MVDC upon distance protection devices.

### 3.1 Network Modelling

Network modelling is undertaken using SimPowerSystems components within Simulink. The network section being modelled is based upon small section of 33 kV network found in the south of Scotland where the introduction of embedded

MVDC links has been shown to be promising through simulation [14]. Lines are modelled as lumped element R-L impedances and neglect negative sequence components.

The X/R ratio of the source is 7 for all studies reported in this paper. The system fault infeed level (FL) is 1000 MVA which is in line with GB DNOs' design limit [15].

### 3.2 Converter Modelling

The converter has a notional power rating of 30 MVA operating under a bipolar configuration with voltages of  $\pm 27$  kV. This voltage is based upon the peak ac phase to ground voltage expected under normal operating scenarios in a 33 kV ac network as outlined in equation (1).

$$V_{dc} \geq \frac{\sqrt{2} \times V_{LLRMS}}{\sqrt{3}} \quad (1)$$

The converter used for studies is a two level converter which includes protection functionality in line with strategies outlined in section 2.2 [16]. The per-unit impedance of the converter is 50 % (referred to the converter base impedance). Note that this high impedance was used to take the converter transformer [15], filter and arm inductance into account. Power electronic modelling is conducted within the Simulink environment.

### 3.3 Dynamic Protection Modelling Controller

The Dynamic Protection Modelling Controller (DPMC) [17] software package, developed within the University of Strathclyde, is used to determine the impedance of the network seen during steady state and fault conditions. This software was developed to assist National Grid in post fault investigation of power system performance under various transient faults and events.

The software takes time stamped three phase voltages and current values along with a relay setting file. The software calculates which of the distance protection comparators operate and the associated activation time during the system event. DPMC will provide the tripping time of the relay according to the setting file logic. The software also generates an R-X diagram of the event for visual interrogation of the impedance locus.

The GEC Measurements SHNB MicroMho distance protection relay model will be deployed for all simulations in this paper.

### 3.4 Methodology

Figure 4 outlines the steps carried out to model the impact of introducing embedded MVDC links upon distance protection schemes.

Based upon the information gathered from DPMC, recommendations can be made to alleviate potential degradation of distance protection performance. Subsequently, any such performance adjustments can be implemented and

repeated in DPMC thus allowing such recommendations to be validated.

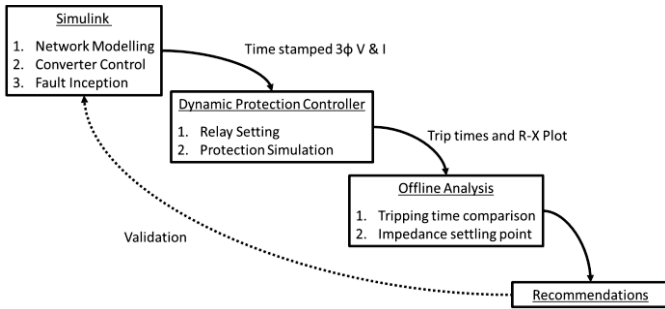


Figure 4: Simulation flow diagram

### 3.5 Study Limitations

It needs to be noted that there are a few limitations to the study. The network model only considers positive sequence lumped R-L element transmission line models rather than a full sequence component model. This is acceptable for this study as three phase symmetrical faults are considered only, but a more detailed line model would be required to study unsymmetrical faults.

Converters are limited to conventional two level voltage source converters (VSC) schemes rather than modular multi-level converter (MMC) style devices. However, it is envisaged that MMC half bridge converters will exhibit a similar ac side response to those modelled due to the diode arrangement within the cells.

## 4 Network Studies

Two main studies are presented in this paper. The first determines whether the reach of a distance protection relay is changed should an embedded dc link with fault ride through capability be introduced. The second study establishes whether the relay can detect a dc side fault. All conductors use the same characteristic impedance of  $(0.17 + j0.3) \Omega/\text{km}$ . The test network is outlined in Figure 5.

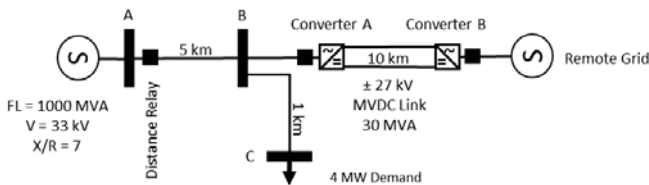


Figure 5: Test network section

### 4.1 Relay Setting Parameters

Relay settings are assigned in accordance with the MicroMho protection manual [18] with zone reach and tripping times specified previously.

A voltage transformer (VT) ratio of 33,000/110 V/V and a current transformer (CT) ratio of 1750/5 A/A are selected. The

CT primary current was selected to minimise the effects of transformer saturation on the results.

The angle ( $\theta$ ) of the mho characteristic is set by considering the ratio between the downstream line reactance and resistance as specified by equation (2).

$$\theta = \tan^{-1} \left( \frac{X_{line}}{R_{line}} \right) = \tan^{-1} \left( \frac{0.3}{0.17} \right) = 60.5^\circ \quad (2)$$

Key distance zone setting parameters are outlined in Table 2 (note that the impedances are secondary referred therefore taking into account the CT and VT turns ratios).

Parameter	Value
Mho Phase ( $^\circ$ )	60.5
Z1 ( $\Omega$ )	1.6
Z2 ( $\Omega$ )	2.4
Z3 ( $\Omega$ )	7.2
Z2 Fault Delay (phase & earth) (s)	0.5
Z3 Fault Delay (phase & earth) (s)	1.3

Table 2: Summary of relay setting parameters

### 4.2 Relay Reach Studies – AC Side

#### A. No converter fault contribution – ac side fault

For benchmarking purposes, a three phase to ground fault was thrown at busbar C after 0.4 s. The load busbar sits between the boundaries of zone 2 and 3. For this study the converter immediately disconnects from the network when a voltage depression is measured. The converter therefore does not contribute any fault current.

The R-X diagram for the fault event is presented in Figure 6 where the fault impedance stabilises in zone 2. Note that axis markers for all R-X diagrams presented are in steps of  $1 \Omega$ . Table 3 (where E indicates a fault to earth and P signifies a phase fault) shows that the relay trips for a zone 2 phase fault at 0.9201 s, 0.52 s after the fault is thrown.

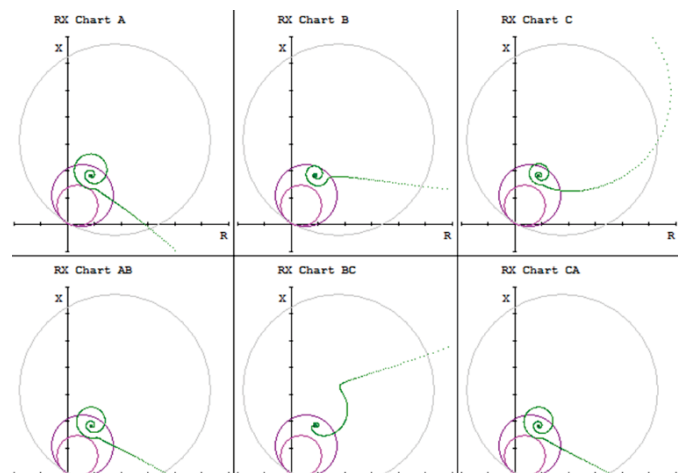


Figure 6: R-X diagram for fault (ABCG) with no infeed



Scenario	Time (s)	Element						Trip Signal
		Z1		Z2		Z3		
		E	P	E	P	E	P	
Without Infeed	0.4179				*			
	0.4201				*		*	
	0.9201				*		*	*

Table 3: Relay log for fault without infeed from MVDC link

### B. Converter fault contribution – ac side fault

From the study conducted it is known that the voltage at the MVDC converter bus bar sits approximately 25 % of nominal during a bolted short circuit. According to the FRT response outlined previously in Figure 3 the converter should remain connected for between 0.5 and 1.1 seconds for such a reduced network voltage. This logic was applied to the converter controller with a random disconnection time between the RFG curves selected.

Table 4 shows that the relay trips for a zone 3 fault in this instance at 1.72 s, 1.32 seconds after fault inception. Figure 7 displays the R-X component of the event where the fault settles just outside zone 2.

Scenario	Time	Element						Trip Signal
		Z1		Z2		Z3		
		E	P	E	P	E	P	
With Infeed	0.4200						*	
	1.7222						*	*

Table 4: Relay log for fault with infeed from MVDC link

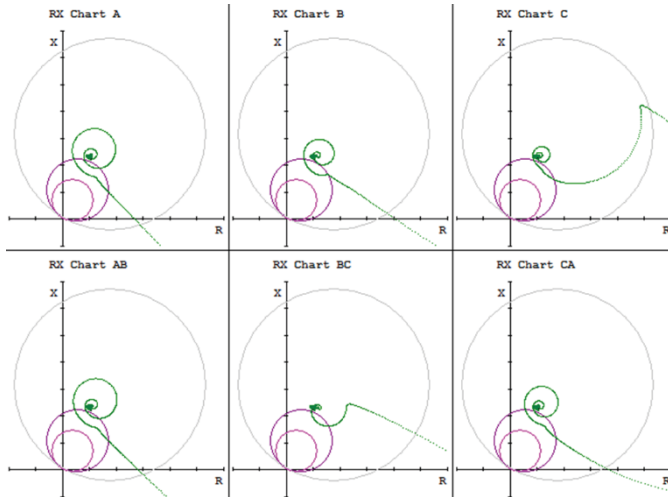


Figure 7: R-X diagram for fault (ABCG) with MVDC infeed

It is observed that the fault ride through characteristic of the MVDC converter used in this study has the ability to support the voltage near a branch ac side fault resulting in the calculated fault zone shifting from zone 2 to zone 3; thus delaying disconnection time by 800 ms.

### 4.3 DC Side Pole to Pole Fault Studies

For this fault type it is assumed that the local MVDC converter fails to break during dc side faults, and the remote converter

disconnects from the ac network within two cycles (40 ms) of fault inception.

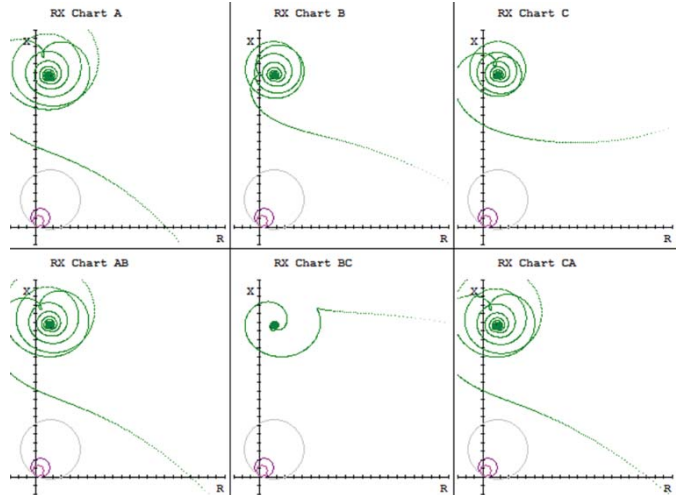


Figure 8: R-X diagram for a pole to pole fault on dc link

The fault trace lies significantly outside of all the protection zones of the distance protection relay. However, the fault does reach a steady state at  $(1.5 + j17.5) \Omega$ .

The steep angle associated with the impedance locus is due to the significant inductance placed between the ac and dc systems used to protect the MVDC converter during dc side faults but also to act as a harmonic filter.

It is hypothesised that a zone 4 setting of a modern digital relay configured with quadrilateral characteristic may be able to provide sufficiently quick backup protection to an embedded MVDC link for a dc side fault.

## 5 Discussion and Future Works

From the studies conducted in this paper it is proposed that a form of accelerated distance protection may offer an innovative method of backing up main converter protection for dc side faults. While further work is required to validate this approach fully across multiple operating scenarios, initial findings appear promising.

When considering ac side faults it is observed that the converter can cause the relay to incorrectly identify fault location due to the raised feeder voltage (distance relay under-reach). This has the potential to increase fault clearance times which is in line with other published studies looking at the effects of distributed generation (DG) upon distance protection schemes [19].

It is worth noting that faults which are in series with the MVDC converters are unlikely to affect clearance times as the voltage collapse associated with the fault results in the phase locked loop controller of the converter losing synchronism, thus disconnecting the link from the network.

A wider range of operating conditions must also be considered to ensure the sensitivity (the ability to identify faults on the system which appear close to normal operating conditions) of the proposed protection approach. Hardware in the loop simulations are currently undergoing investigation using an RTDS (Real Time Digital Simulator) system housed within the University of Strathclyde.

While these studies have been conducted upon a distribution network the findings could also potentially be translated to HVDC transmission scale protection.

For MVDC to be a viable solution to unleashing capacity on the medium voltage networks the modularity of the converters needs to increase and standardise to reduce costs. Remote backup protection of an MVDC link is likely to give grid operators more confidence when deploying such a solution upon their networks.

## 6 Conclusion

This paper has presented a methodology to investigate potential impacts from MVDC links on power system protection performance.

The need for increased control in distribution networks is widely accepted. The role and effectiveness of existing network protection strategies must be validated for topologies where power electronic links are introduced. Modelling via software has identified that the reach of a distance relay could be adversely affected by embedded links.

Through studies presented in this paper it has also been shown that conventional distance protection has the ability to observe faults upon a dc link should the converter topology not have dc fault blocking capability.

The use of existing distance protection relays to act as a backup protection for embedded MVDC links may be attractive for the future DSO. Further studies are currently being carried out both in real-time and offline simulation environments to verify further the appropriateness of using conventional ac distance relays as backup protection for an embedded MVDC link.

## Acknowledgements

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